

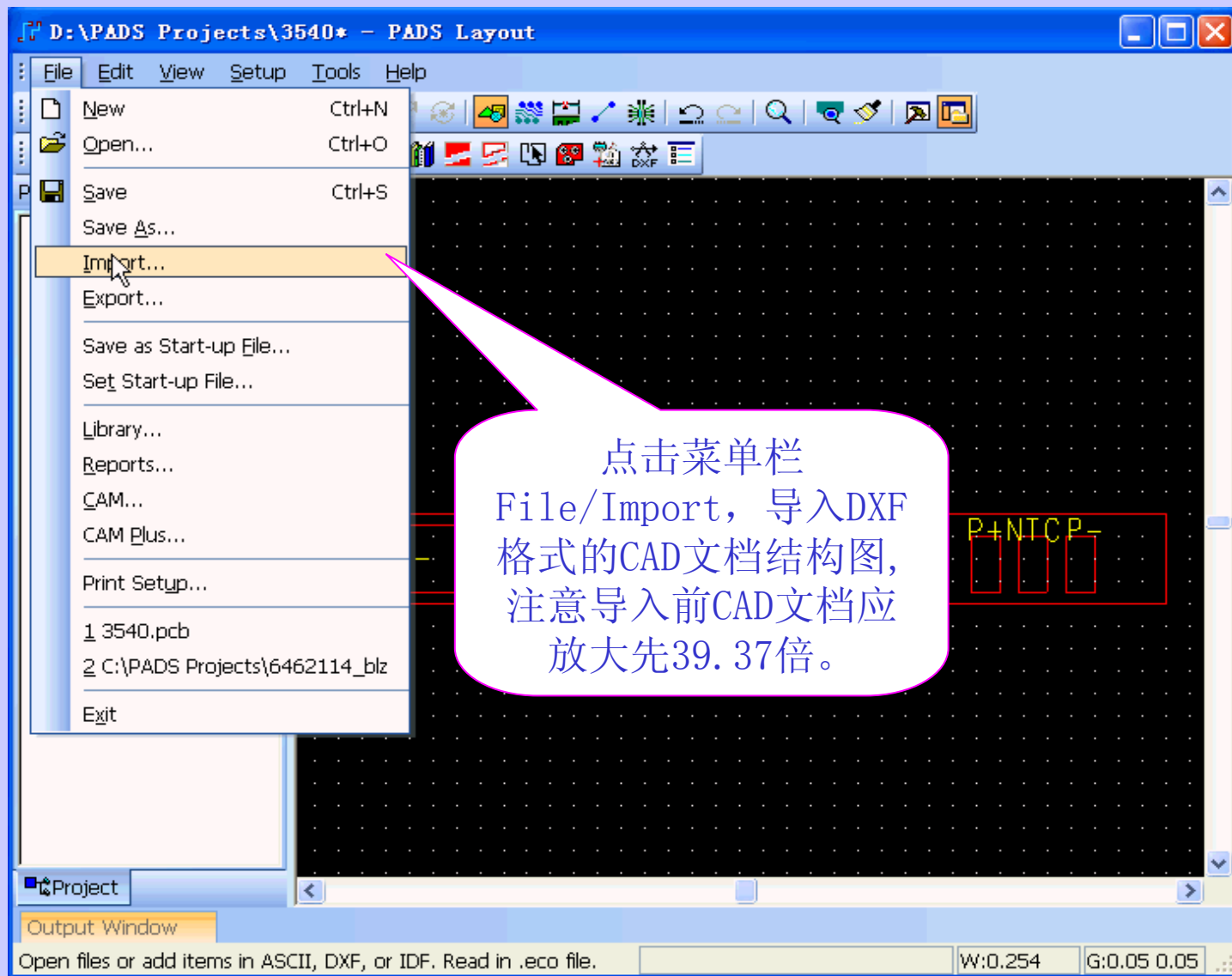


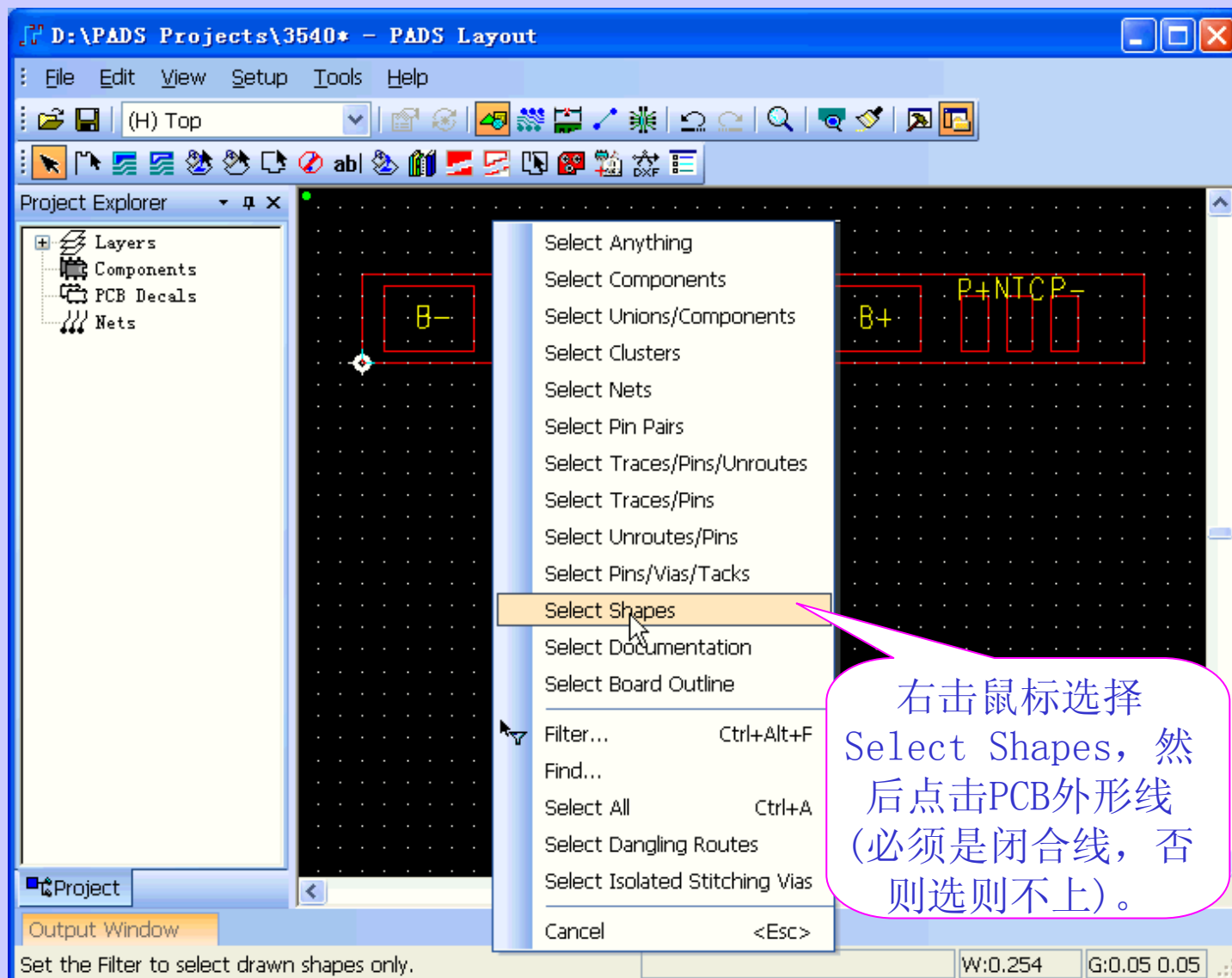
PADS Layout入门教程

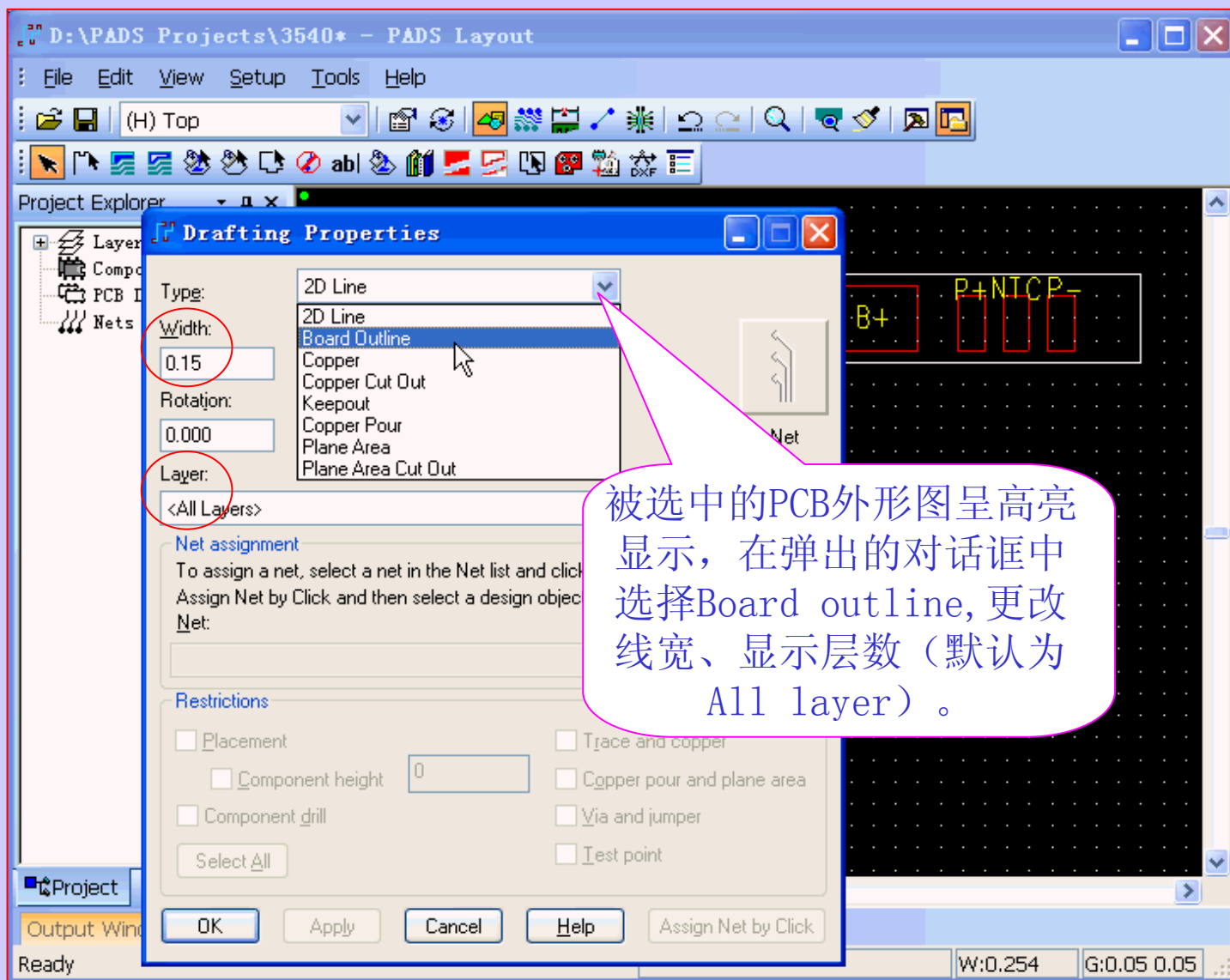
——以PADS2007为例

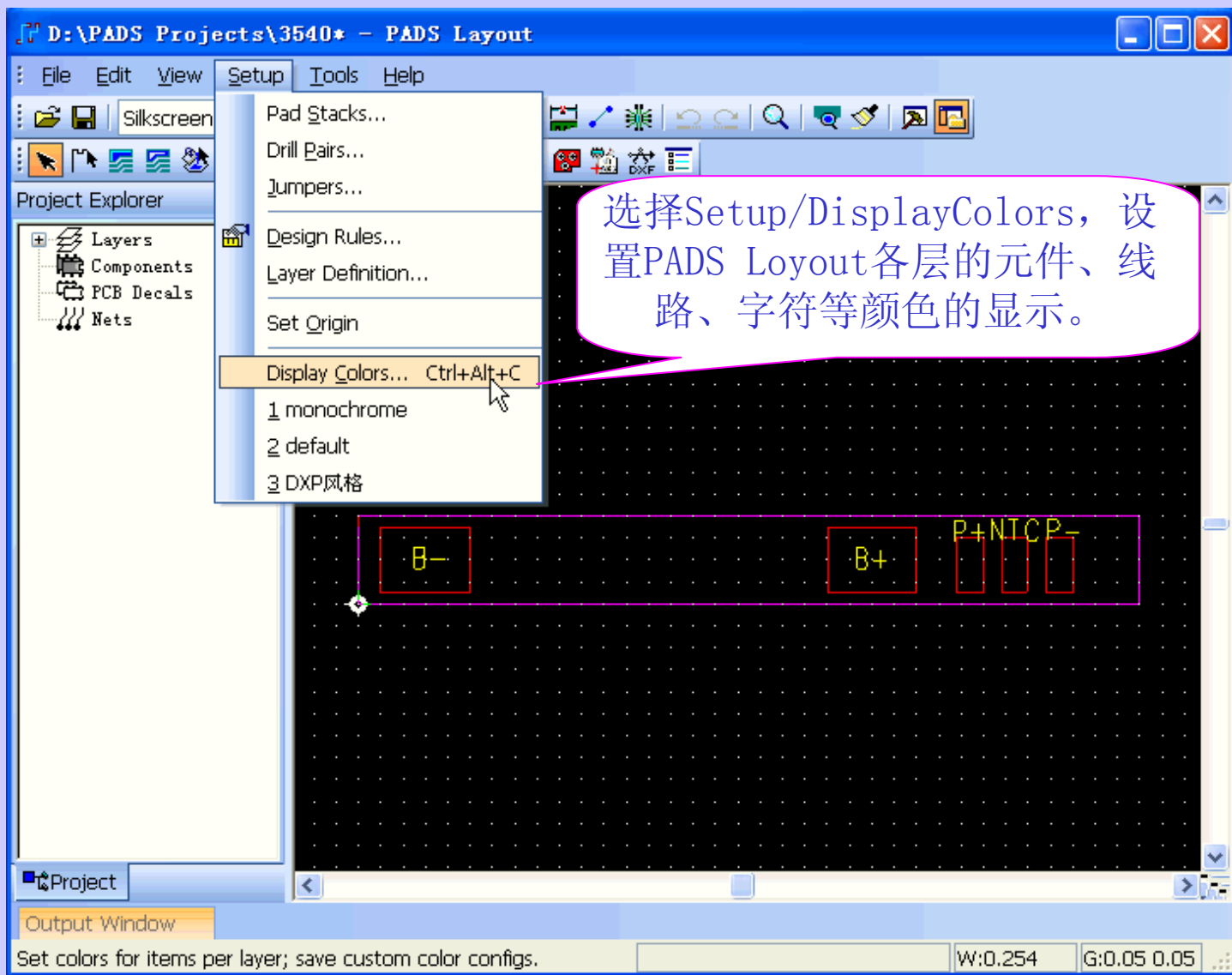


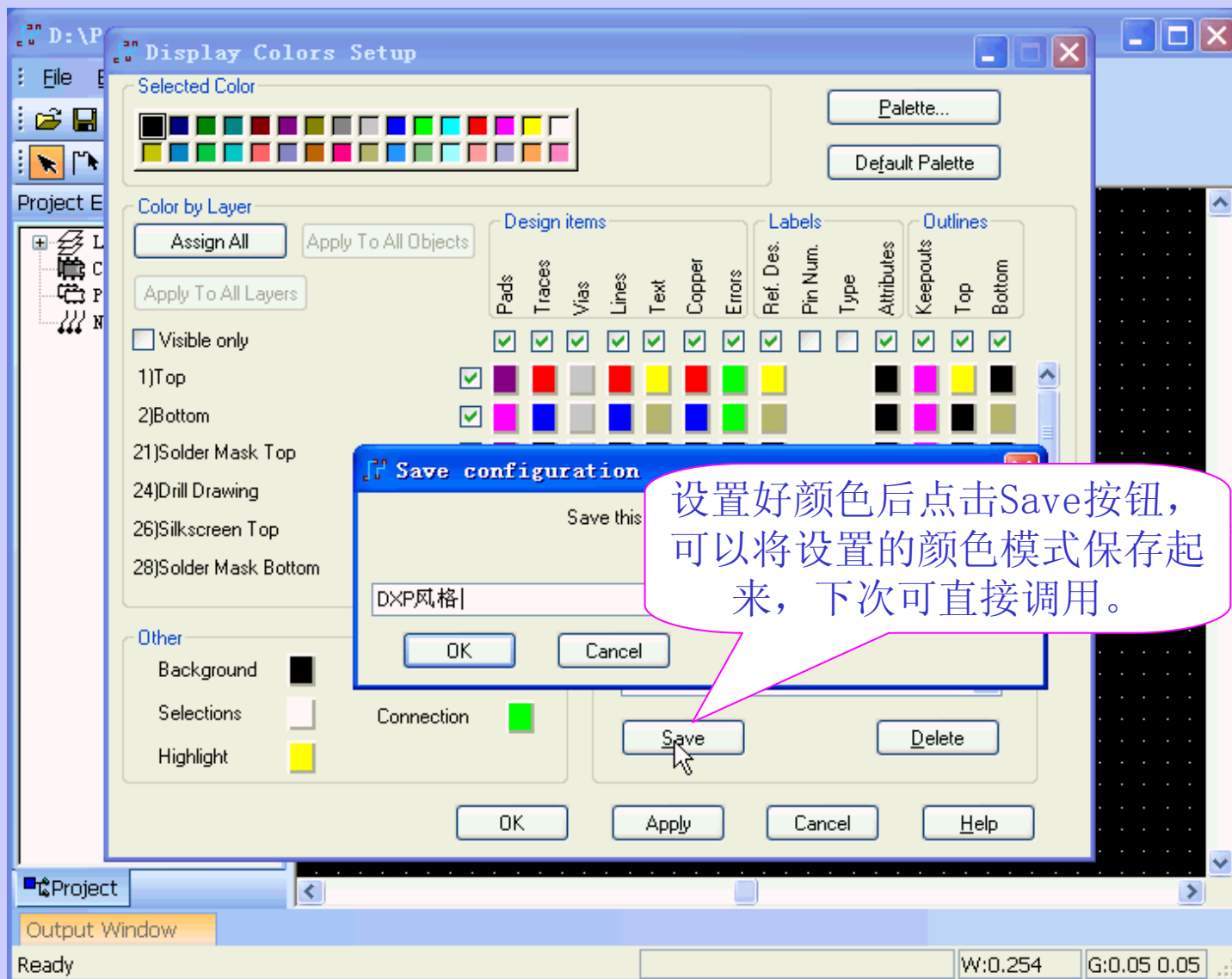
1.导入并设置PCB结构

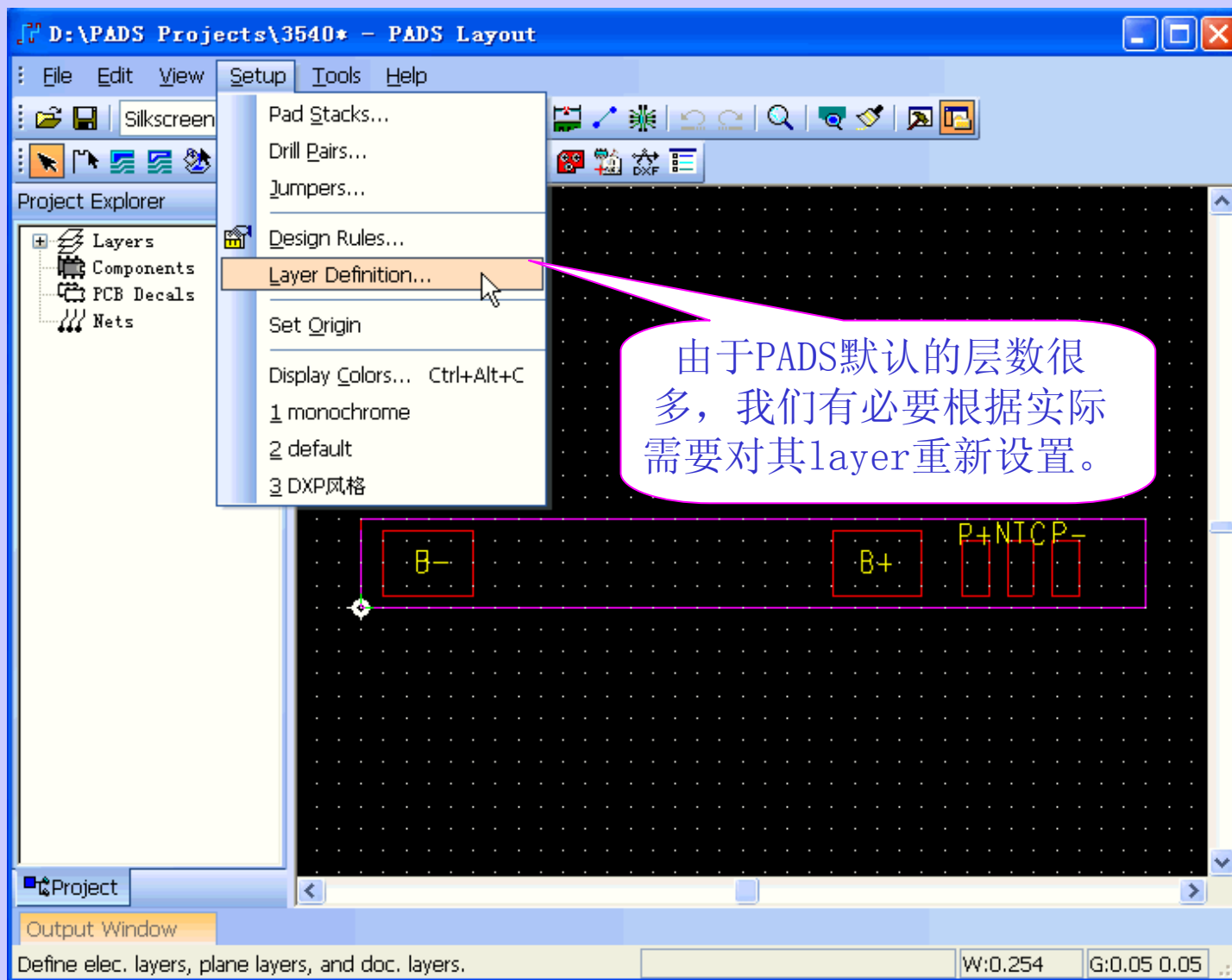


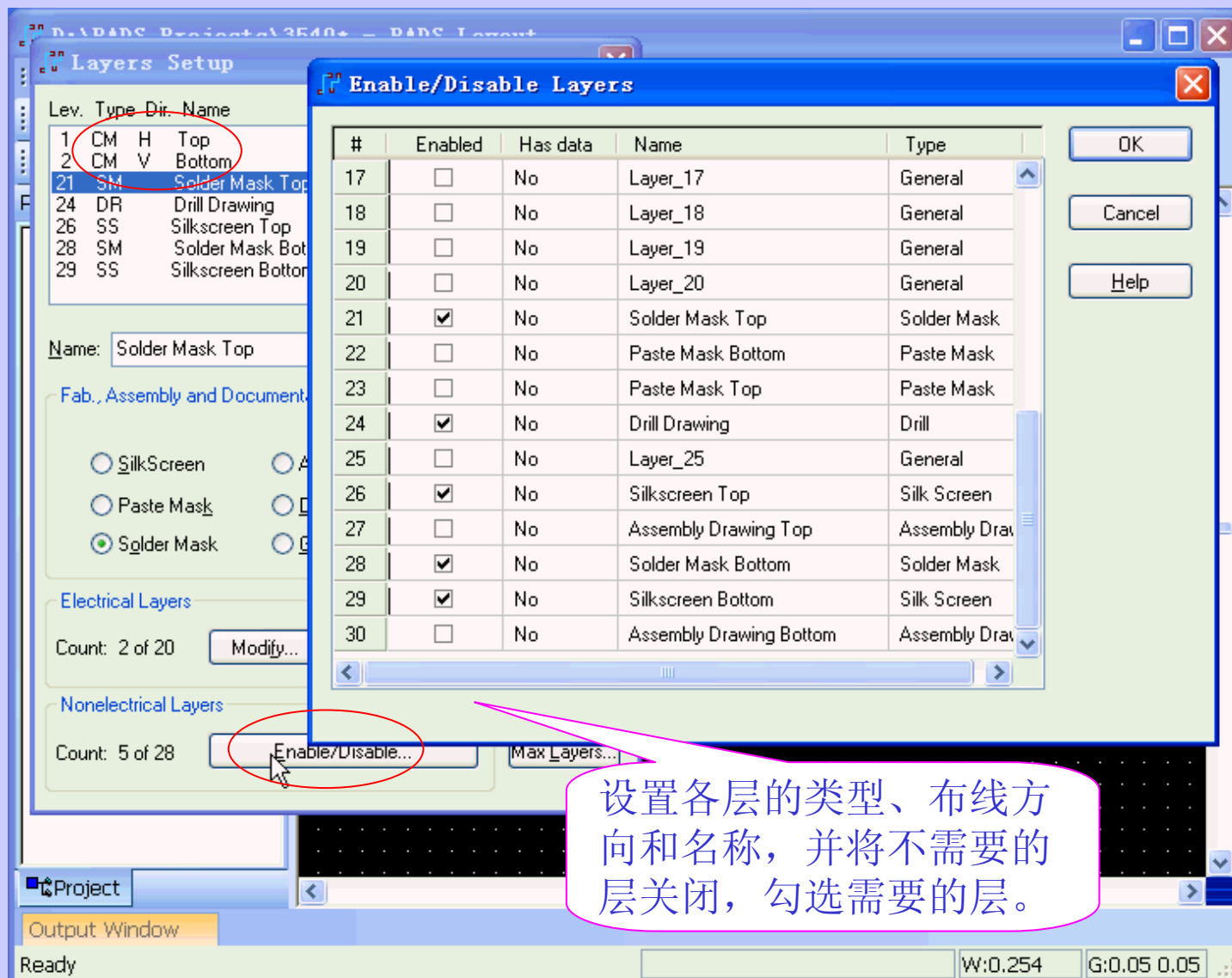










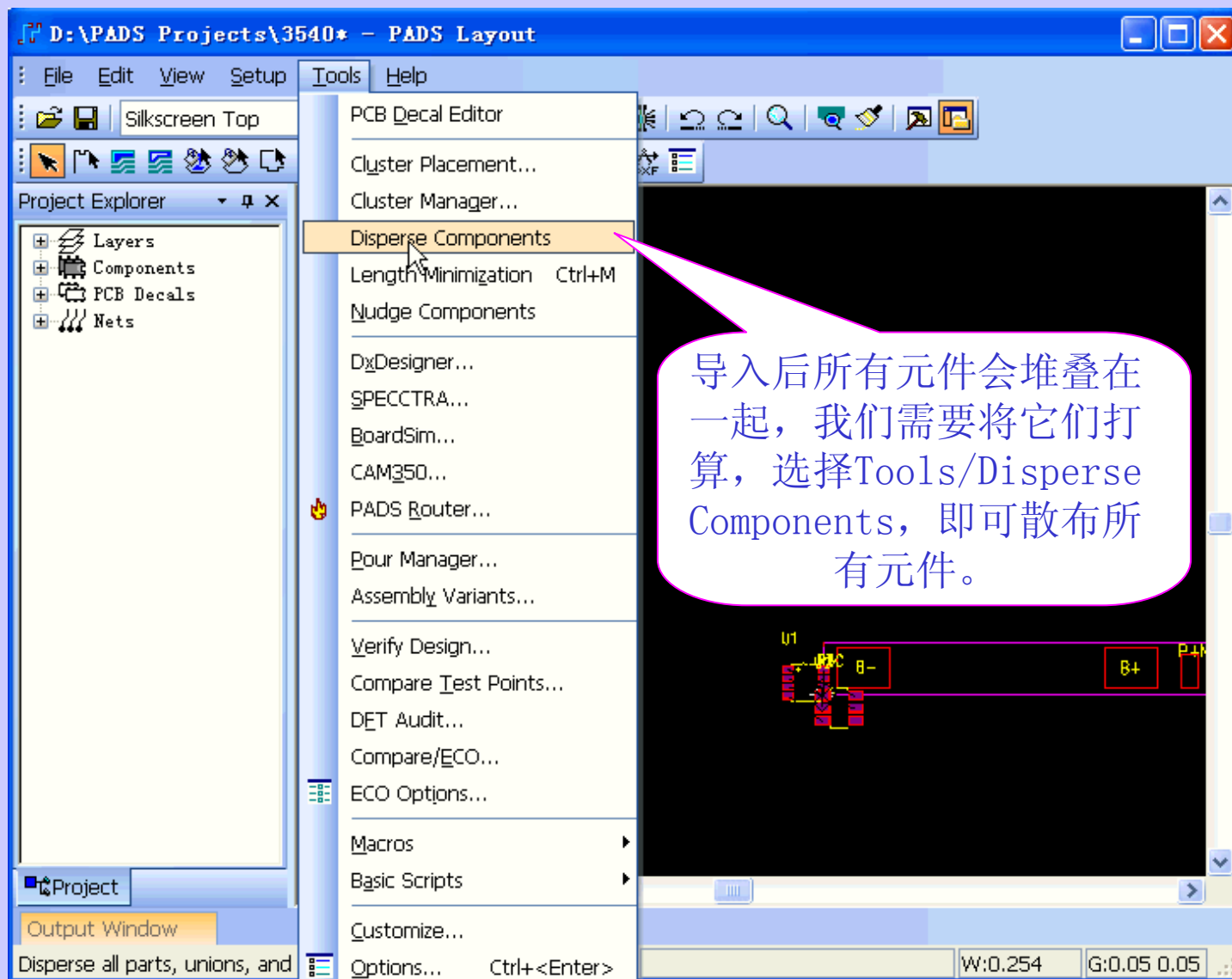


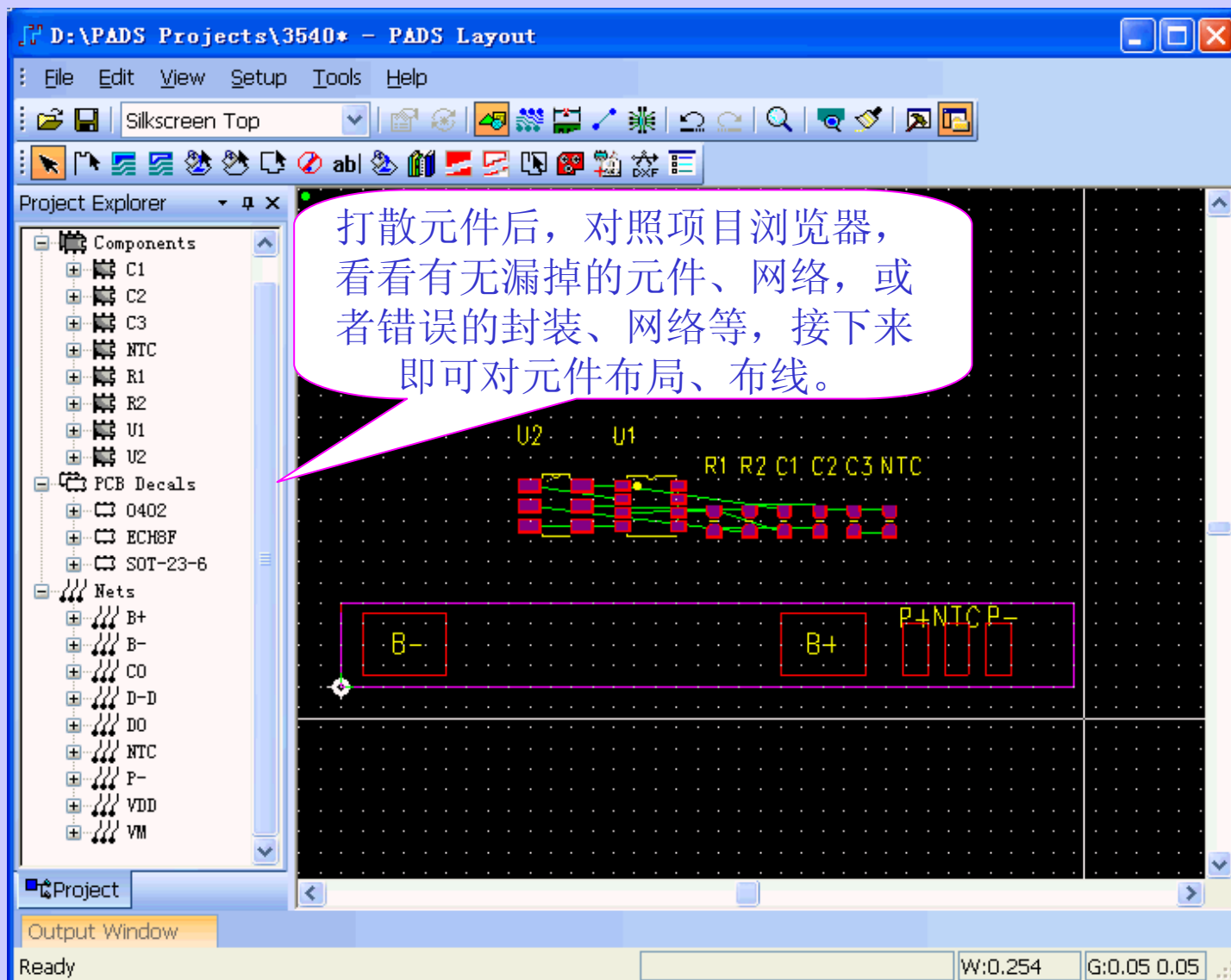


2.From Logic To Layout

The screenshot shows the PADS Logic software interface. The main window displays a logic circuit diagram. The 'Tools' menu is open, showing options like 'Part Editor', 'Update Off-page from Library', 'Update Pins from Library', 'Save Off-page to Library', 'Compare/ECO...', 'Layout Netlist...', 'SPICE Netlist...', 'PADS Layout...' (highlighted with a keyboard shortcut Ctrl+Shift+O), 'PADS Router...', 'Macros', 'Basic Scripts', 'Customize...', and 'Options...' (with a keyboard shortcut Ctrl+<Enter>). A dialog box titled 'PADS Layout Link' is open in the foreground. It has tabs for 'Selection', 'Design', 'Document', 'Preferences', and 'ECO Names'. The 'Design' tab is active, showing options for 'Net list' (Send Net list, Include Design Rules in Net list) and 'Compare/ECO' (Compare Design Rules, Compare PCB, ECO To PCB, ECO From PCB). The 'ECO To PCB' button is highlighted with a mouse cursor. A speech bubble points to the 'ECO To PCB' button with the text: '打开编辑好的原理图，选择Tool/PADS Layout，点击Design/ECO To PCB（或者生成网络表）导入到PCB Layout界面中。'

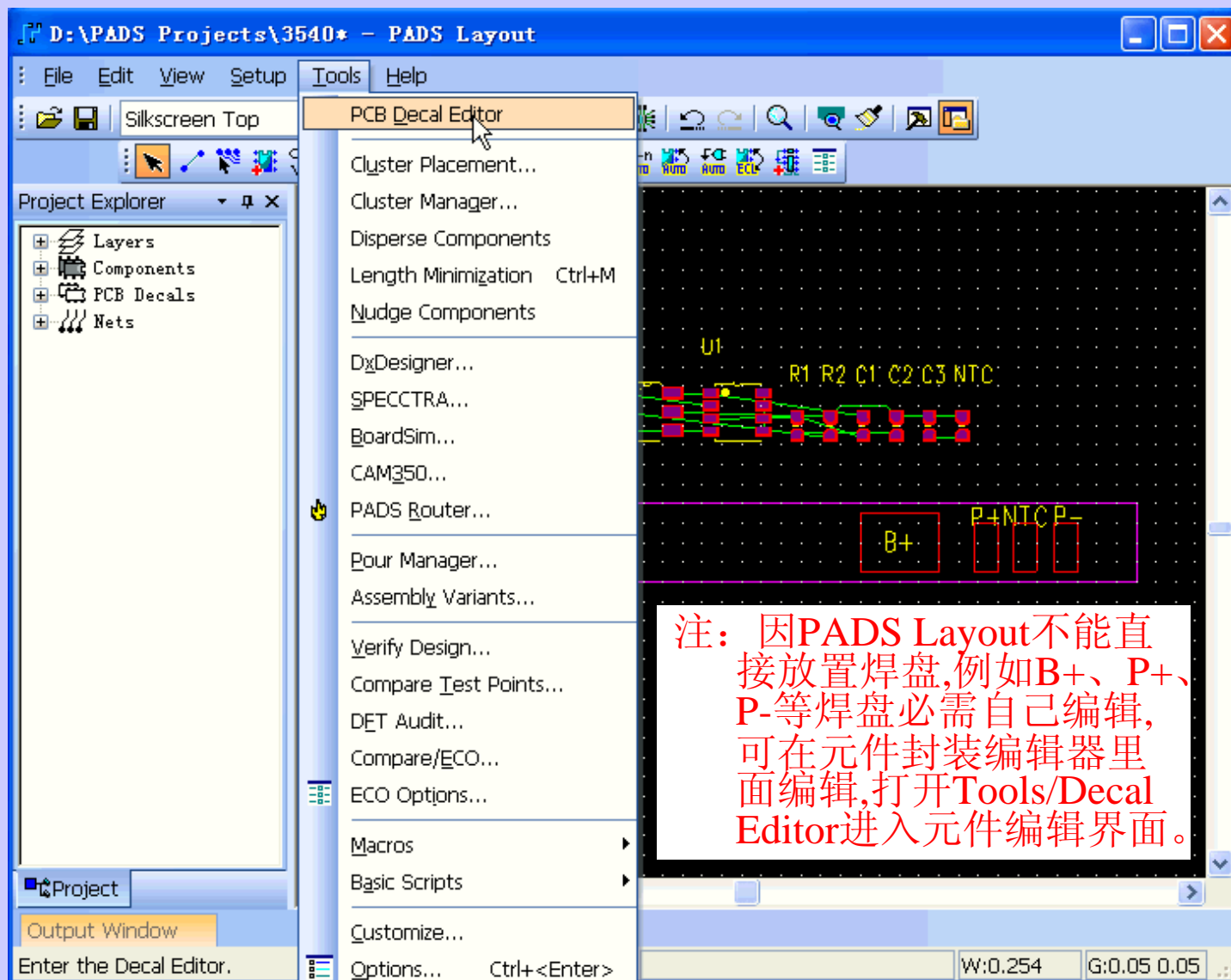
打开编辑好的原理图，选择Tool/PADS Layout，点击Design/ECO To PCB（或者生成网络表）导入到PCB Layout界面中。

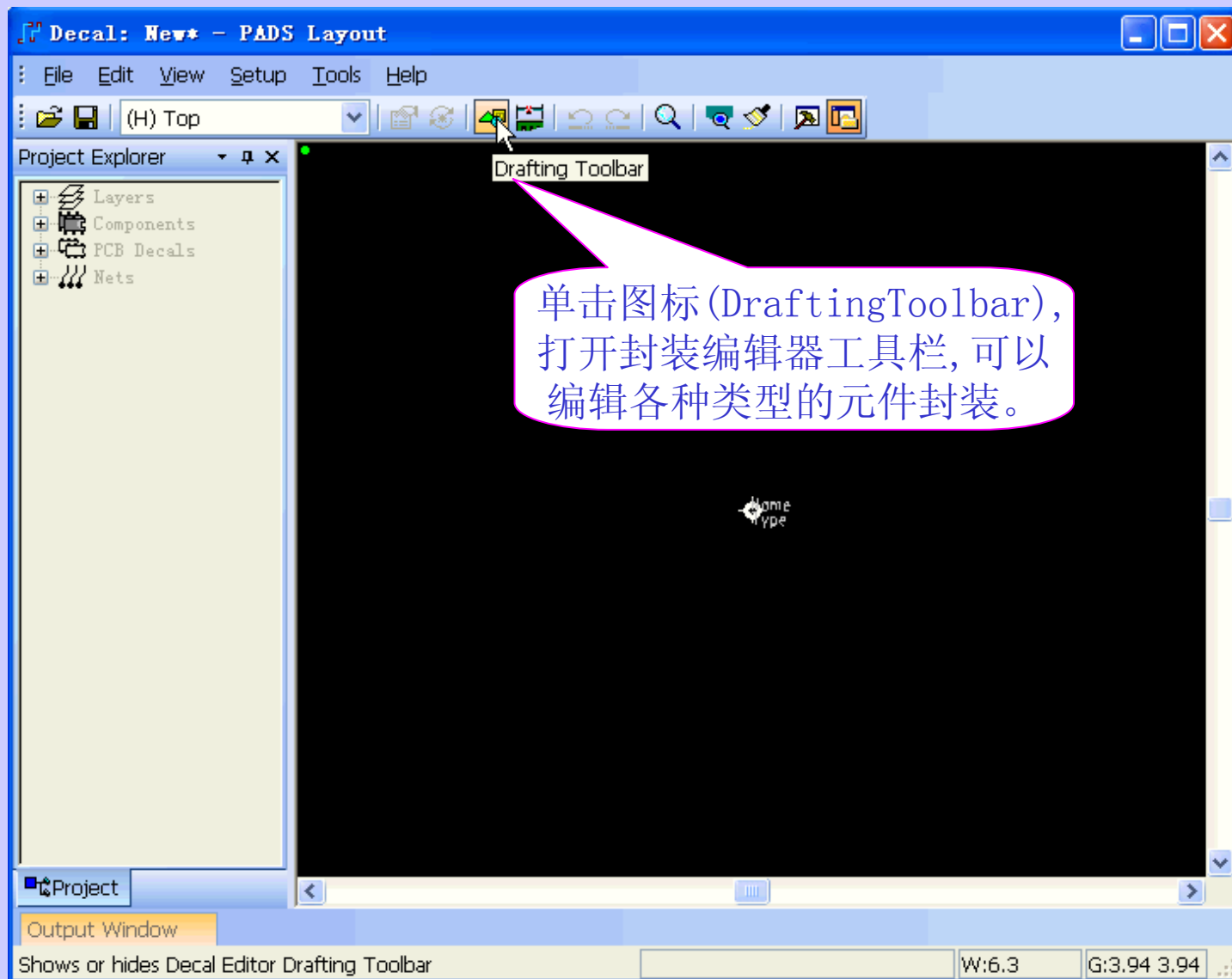


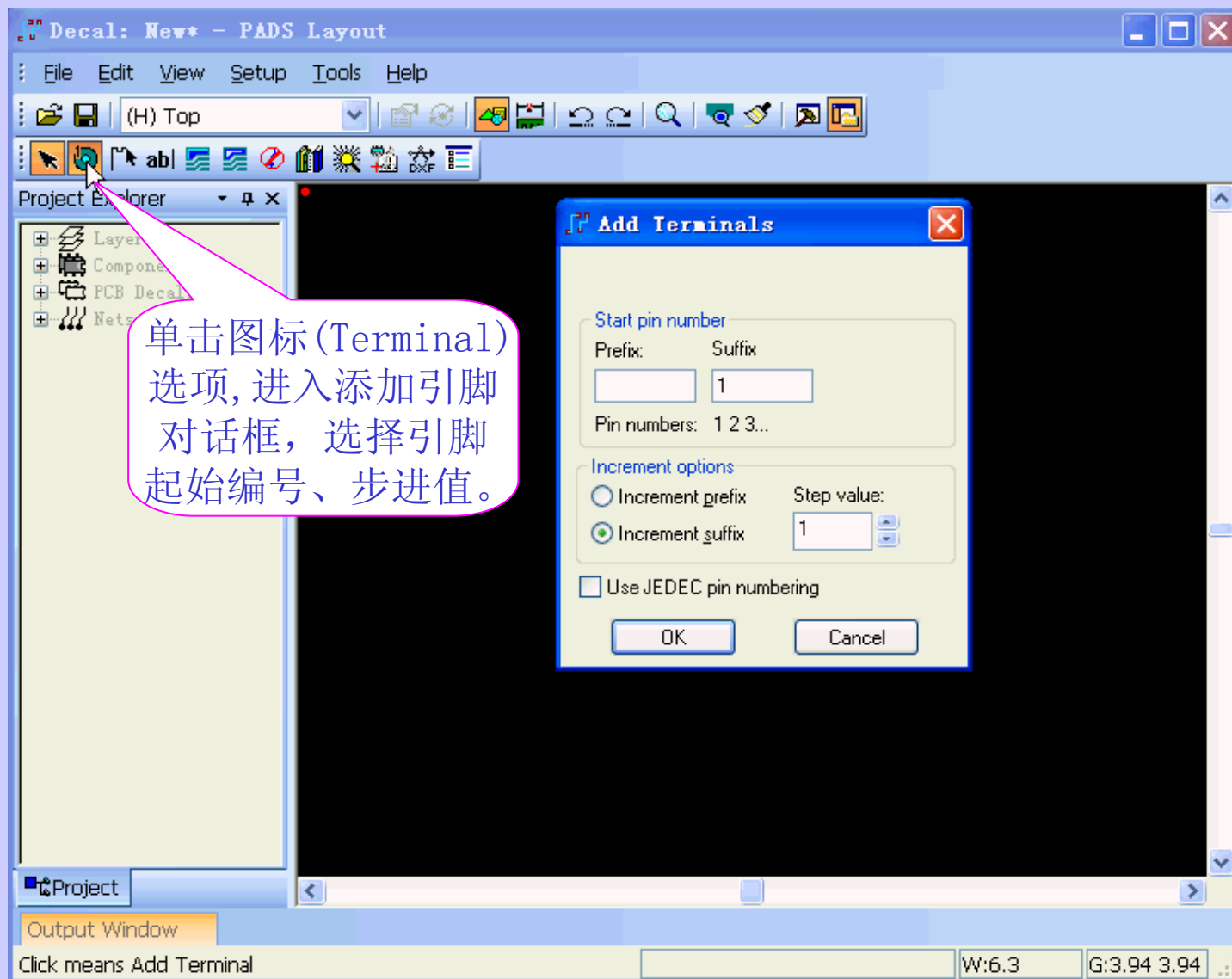


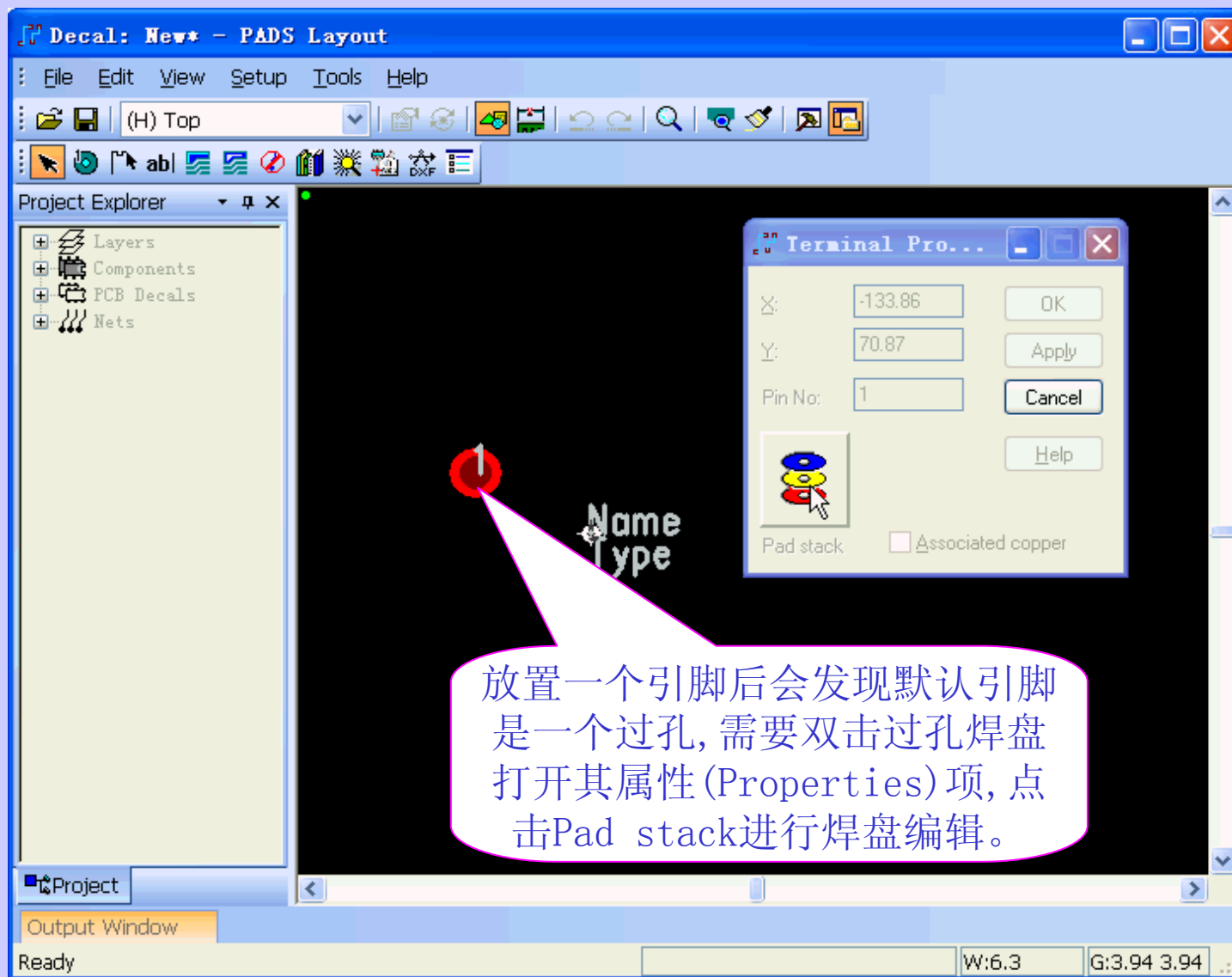


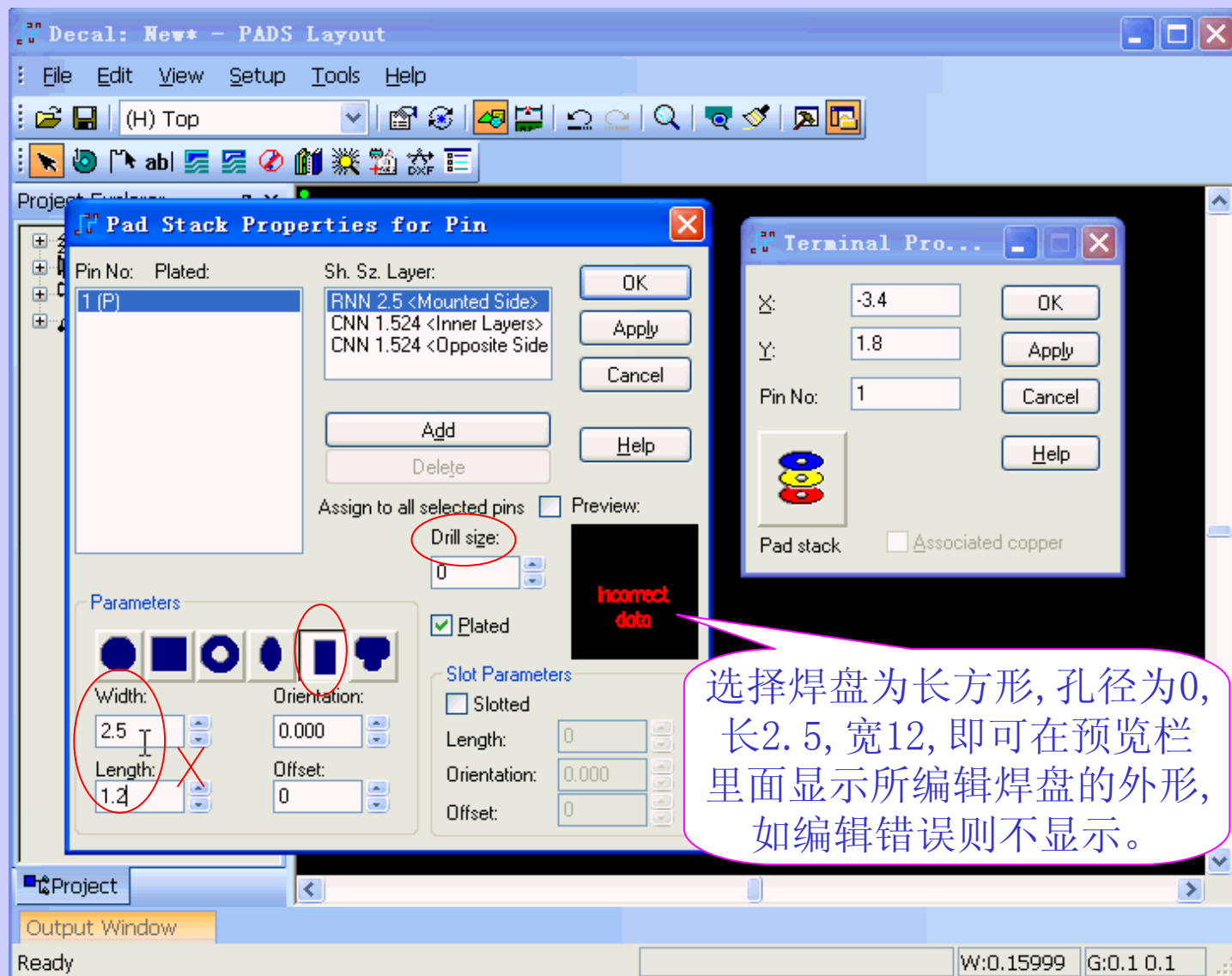
3. 编辑封装库或者PAD

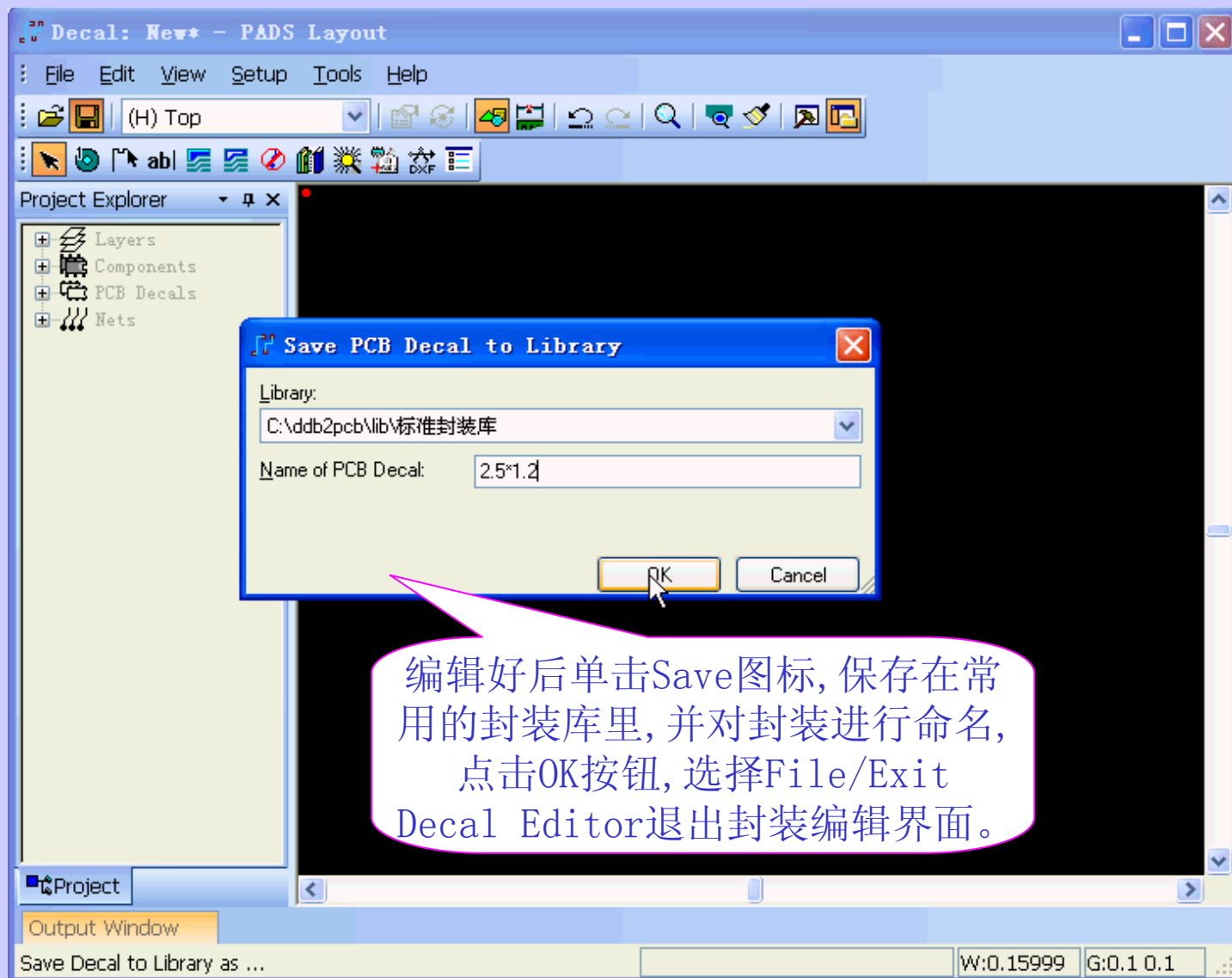














4. 设置布局/布线规则

打开ECO工具栏, 单击Add Component。

在Library下拉菜单里面选择封装库, 输入*, 单击Apply则可显示该封装库所有封装类型, 预览选择所需要的封装, 单击Add添加到PCB Layout中。

Get Part Type from Library - 标准封装库

2.5/1.2

Part Types:

- 16M
- 1812
- 1812PTC
- 1N5408
- 1N5822
- 1N5822A
- 2.5/1.2

Close

Add

Help

Filter

Library:

C:\ddb2pcb\lib\标准封装库

Items:

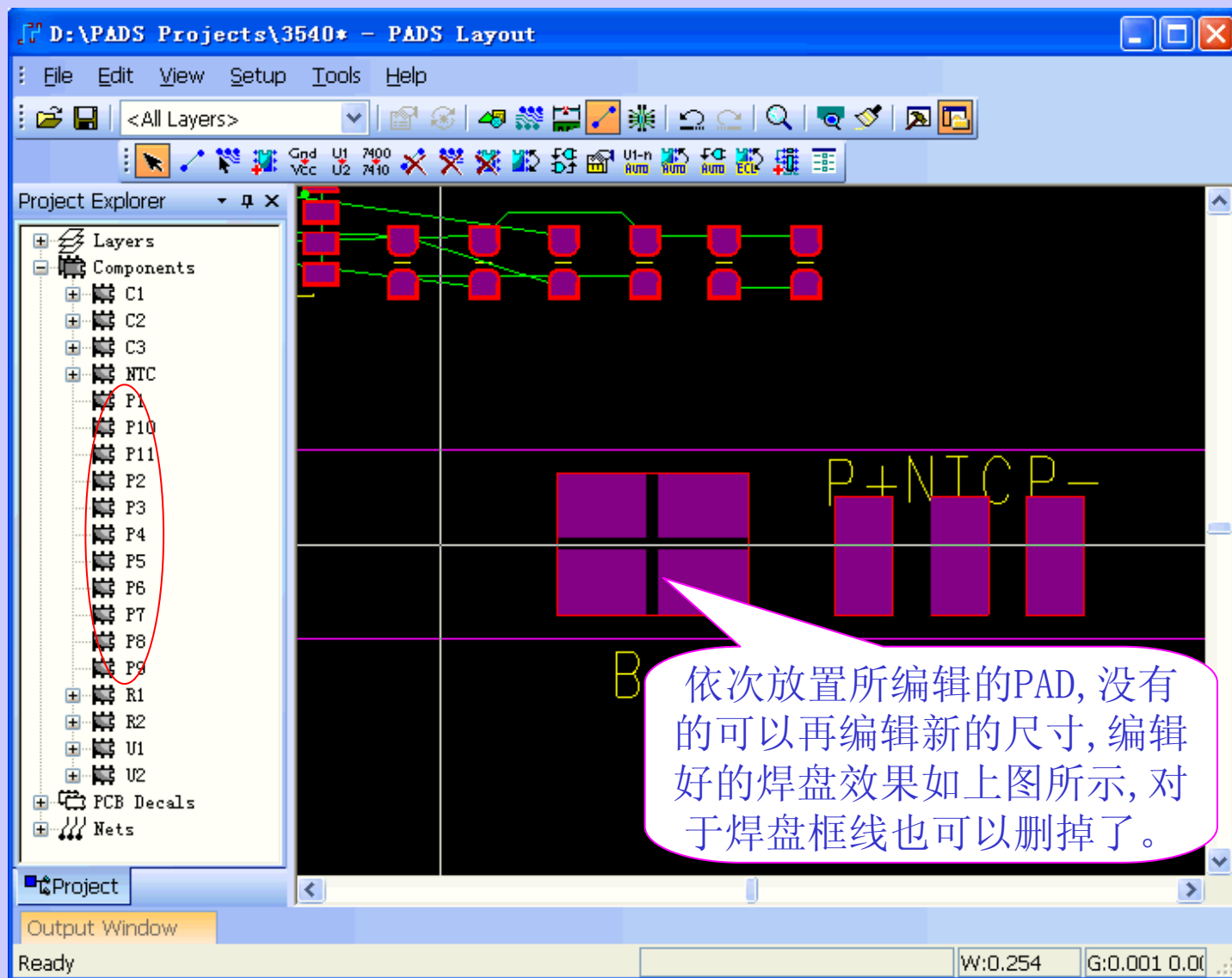
*

Apply

Output Window

Add Part: select Part of desired Type.

W:0.254 G:0.05 0.05





D:\PADS Projects\3540* - PADS Layout

File Edit View Setup Tools Help

<All Layers>

ECO Toolbar

Project Explorer

- Layers
- Components
 - C1
 - C2
 - C3
 - NTC
 - P1
 - P2
 - P3
 - P4
 - P5
 - P6
 - P7
 - P8
 - P9
 - P10
 - P11
 - R1
 - R2
 - U1
 - U2
- PCB Decals
- Nets

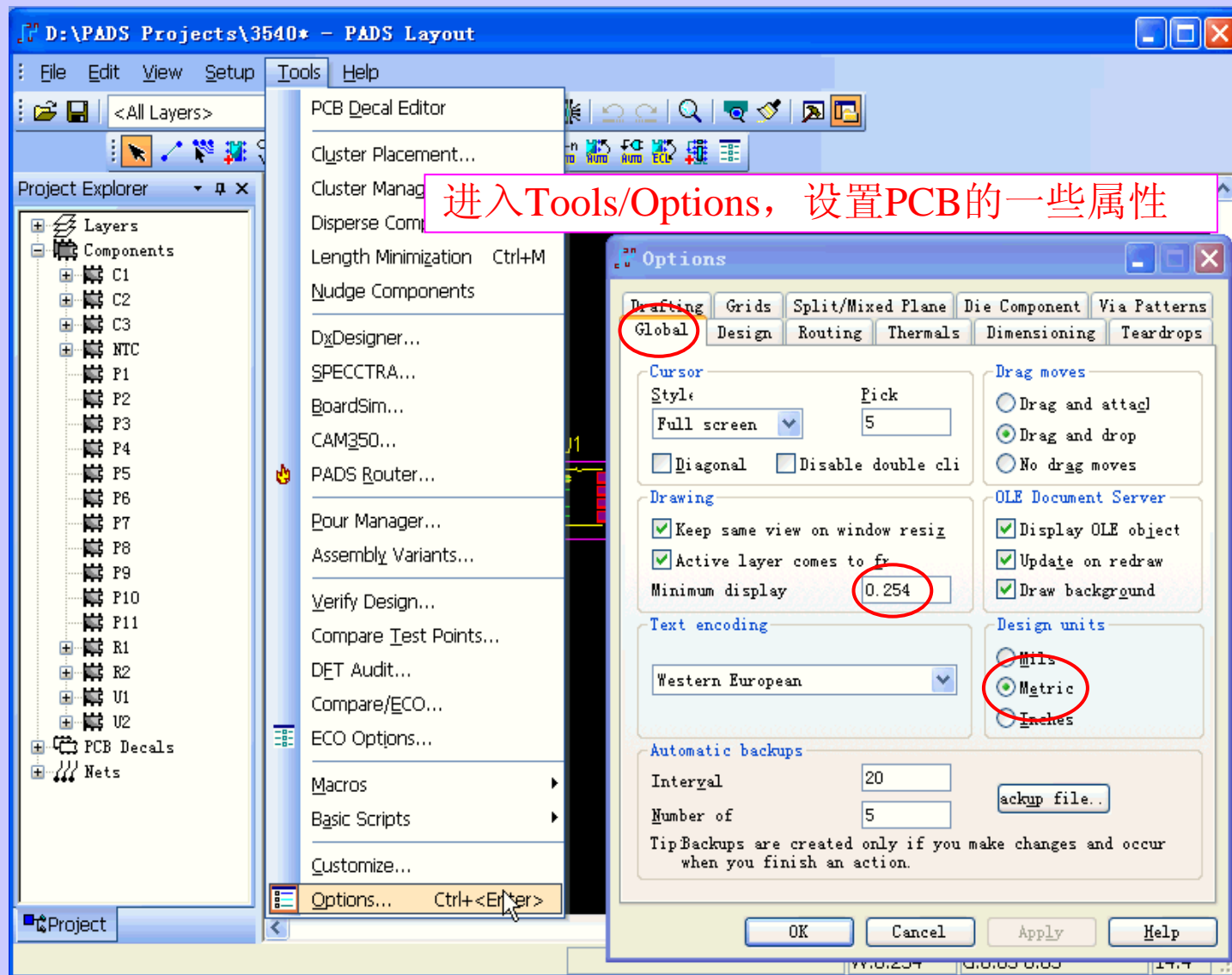
U1 R2 U2 C1 R1 C2 C3 NTC

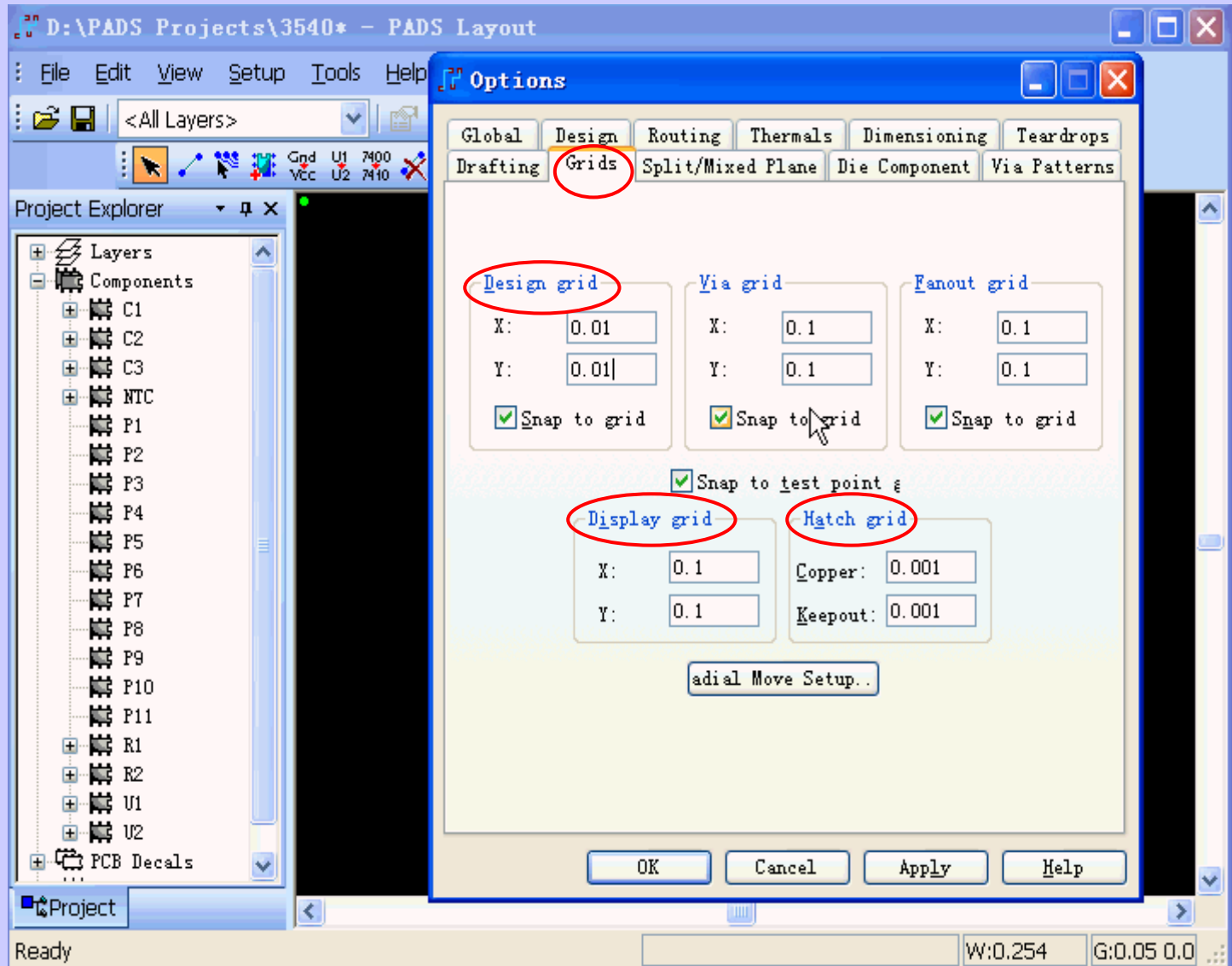
B- B+

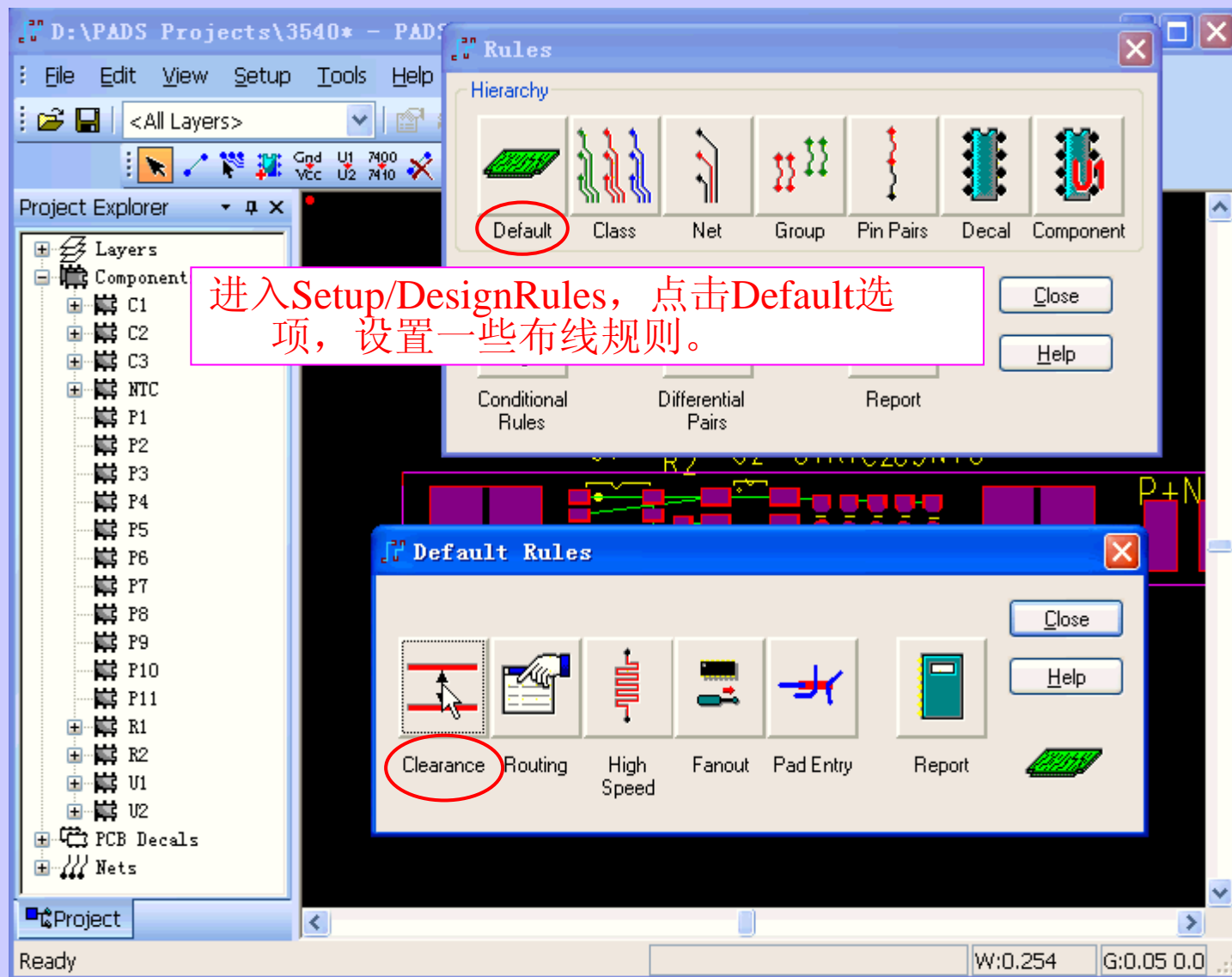
P+ NTC P-

注:移动过程中旋转元件或字符的快捷键为 Ctrl+R,调整显示栅格命令GD *(gd 1),设计栅格G *(gd 0.1),PCB编辑中元件也不可镜像。

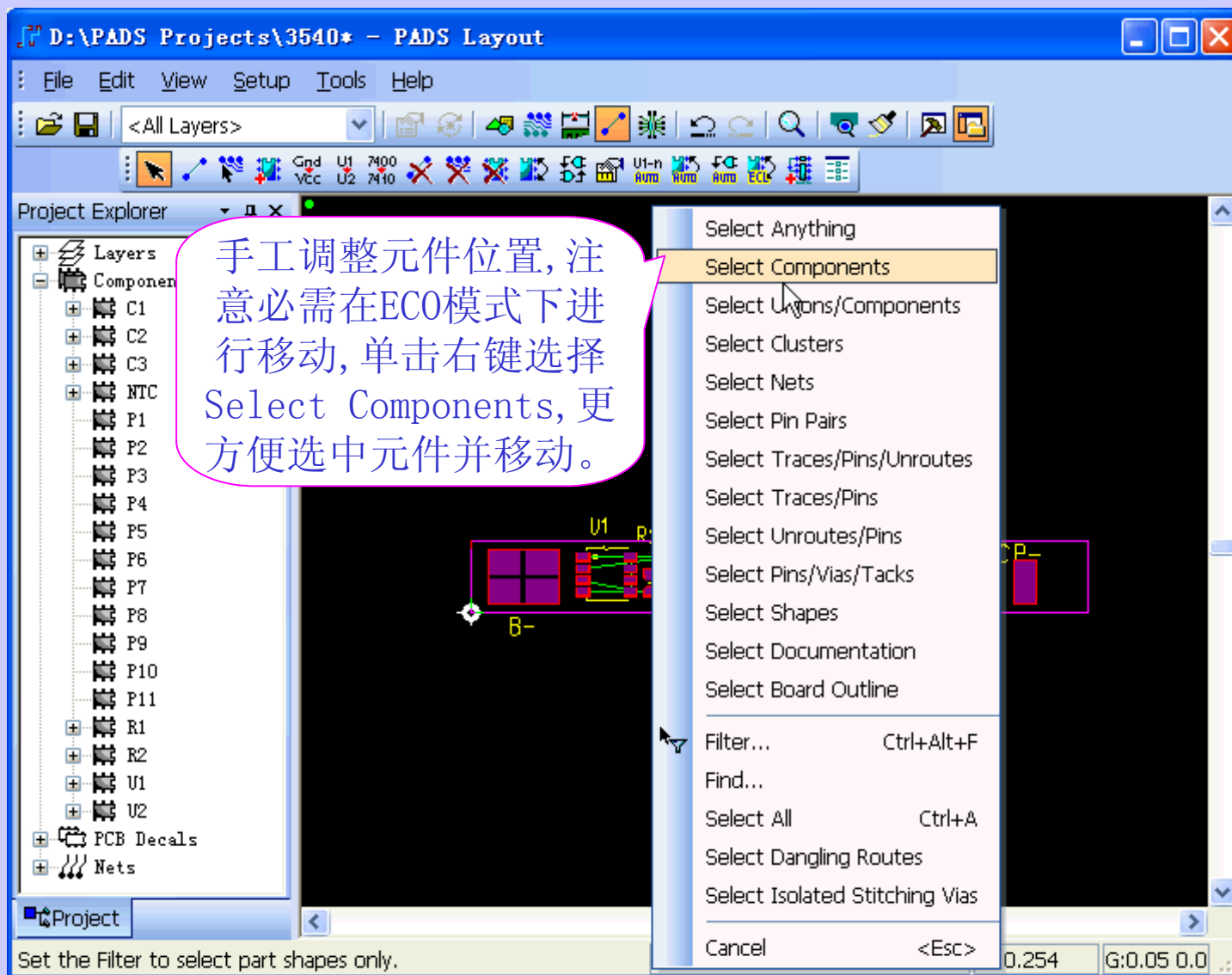
Create or append ECO file. W:0.254 G:0.05 0.05

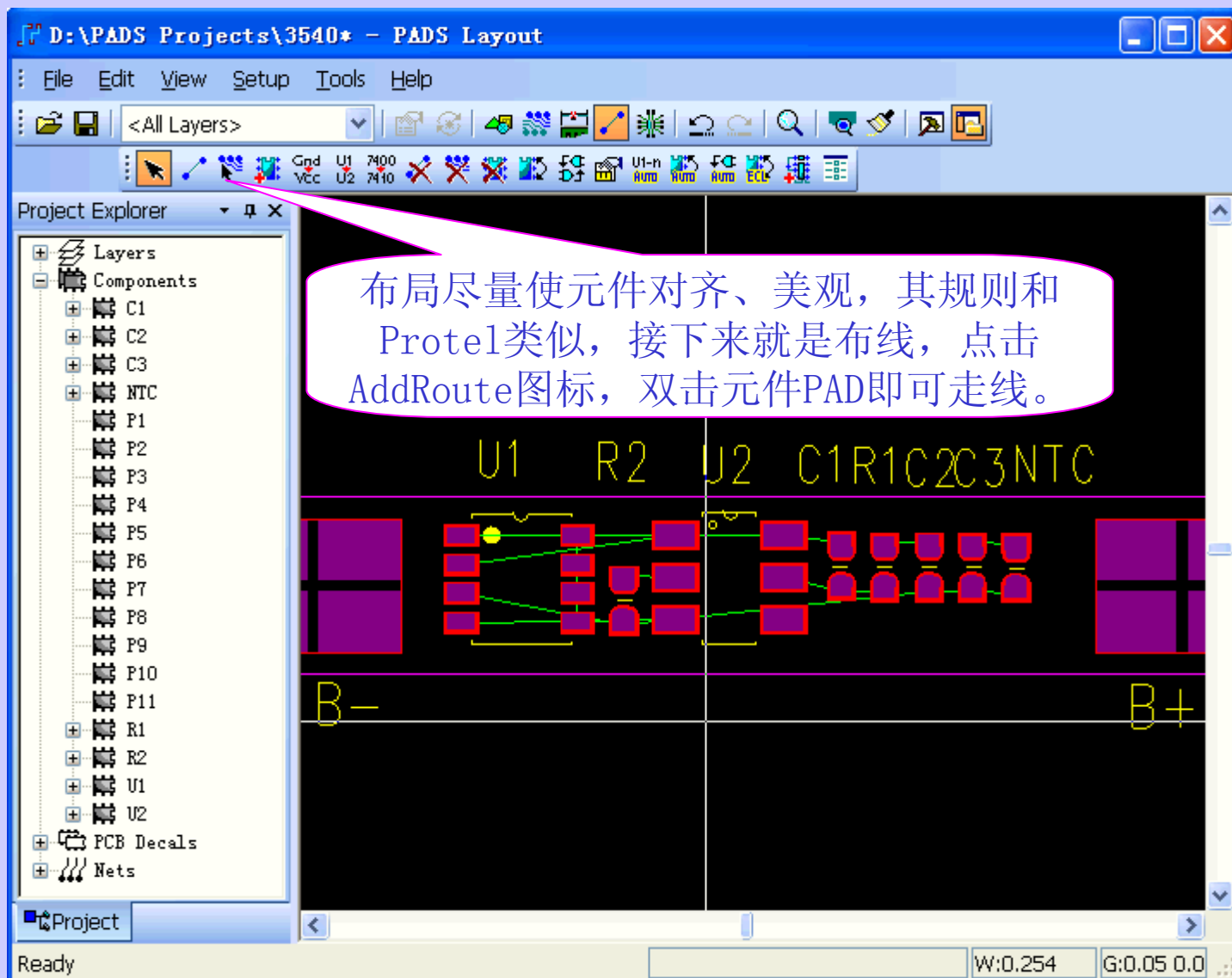














D:\PADS Projects\3540* - PADS Layout

File Edit View Setup Tools Help

(H) Top

Project Explorer

- Layers
- Components
 - C1
 - C2
 - C3
 - NTC
 - P1
 - P2
 - P3
 - P4
 - P5
 - P6
 - P7
 - P8
 - P9
 - P10
 - P11
 - R1
 - R2
 - U1
 - U2
- PCB Decals
- Nets

Modeless Command

Command: w 0.3

WIDTH format: W <n>

Routed: 0.815
Total: 2.61713

VDD, C1.2-U2.5, Routed: 0.815, Total: 2.61713

W: 0.254 D: 0.815 0

默认线宽按照布线规则设定值，要更改线宽可以双击走线或者在走线时输入命令W **即可更改现行线宽。



5.PCB布线/打孔/覆铜

D:\PADS Projects\3540* - PADS Layout

File Edit View Setup Tools Help

(H) Top

Project Explorer

- Layers
- Components
 - C1
 - C2
 - C3
 - NTC
 - P1
 - P2
 - P3
 - P4
 - P5
 - P6
 - P7
 - P8
 - P9
 - P10
 - P11
 - R1
 - R2
 - U1
 - U2
- PCB Decals
- Nets

Pad Stacks...
Drill Pairs...
Jumpers...
Design Rules...
Layer Definition...
Set Origin
Display Colors... Ctrl+Alt+C
1 monochrome
2 default
3 DXP风格

完成基本的连线后，需要打过孔时 要先设置过孔大小和模式。

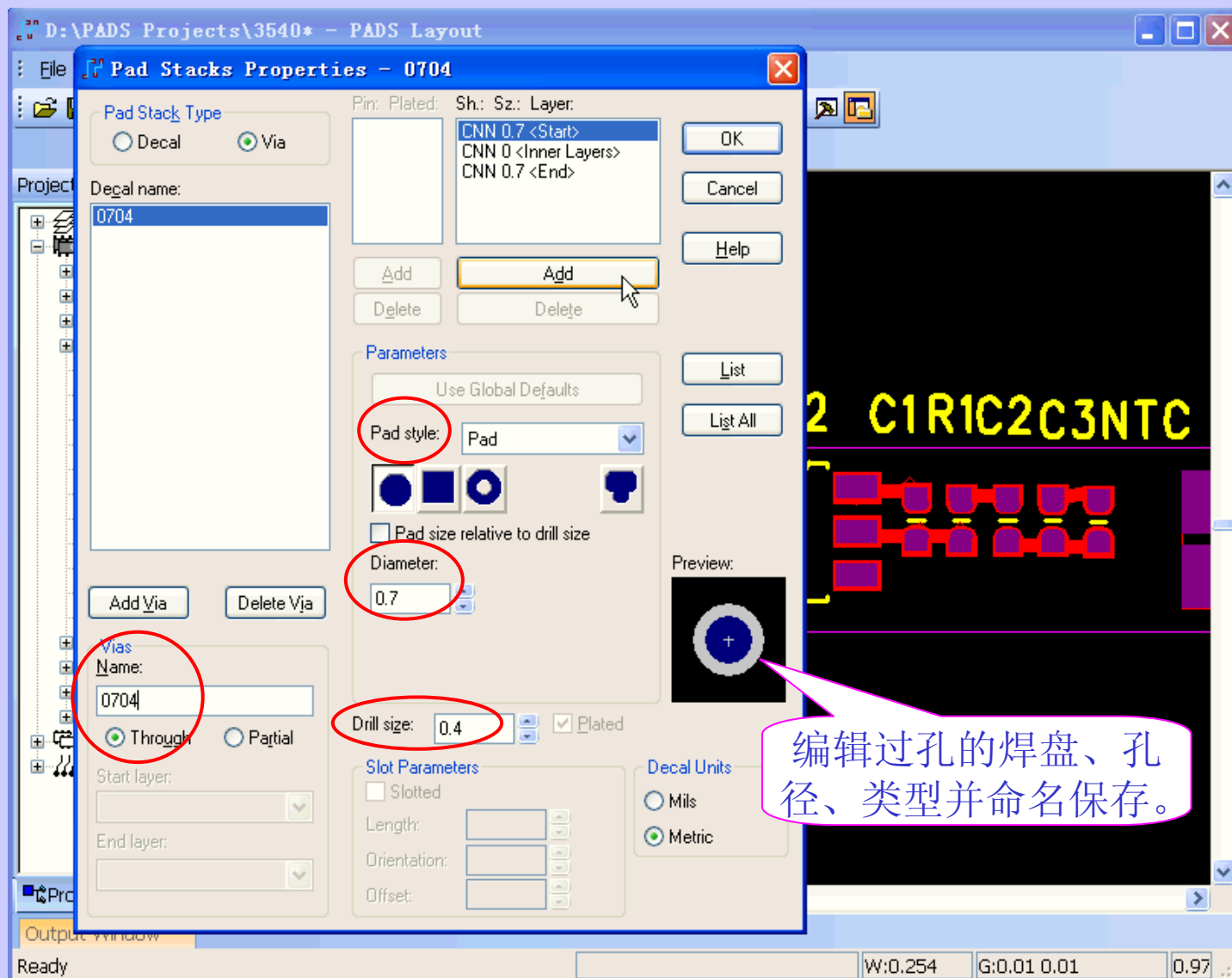
U1 R2 U2 C1 R1 C2 C3 NTC

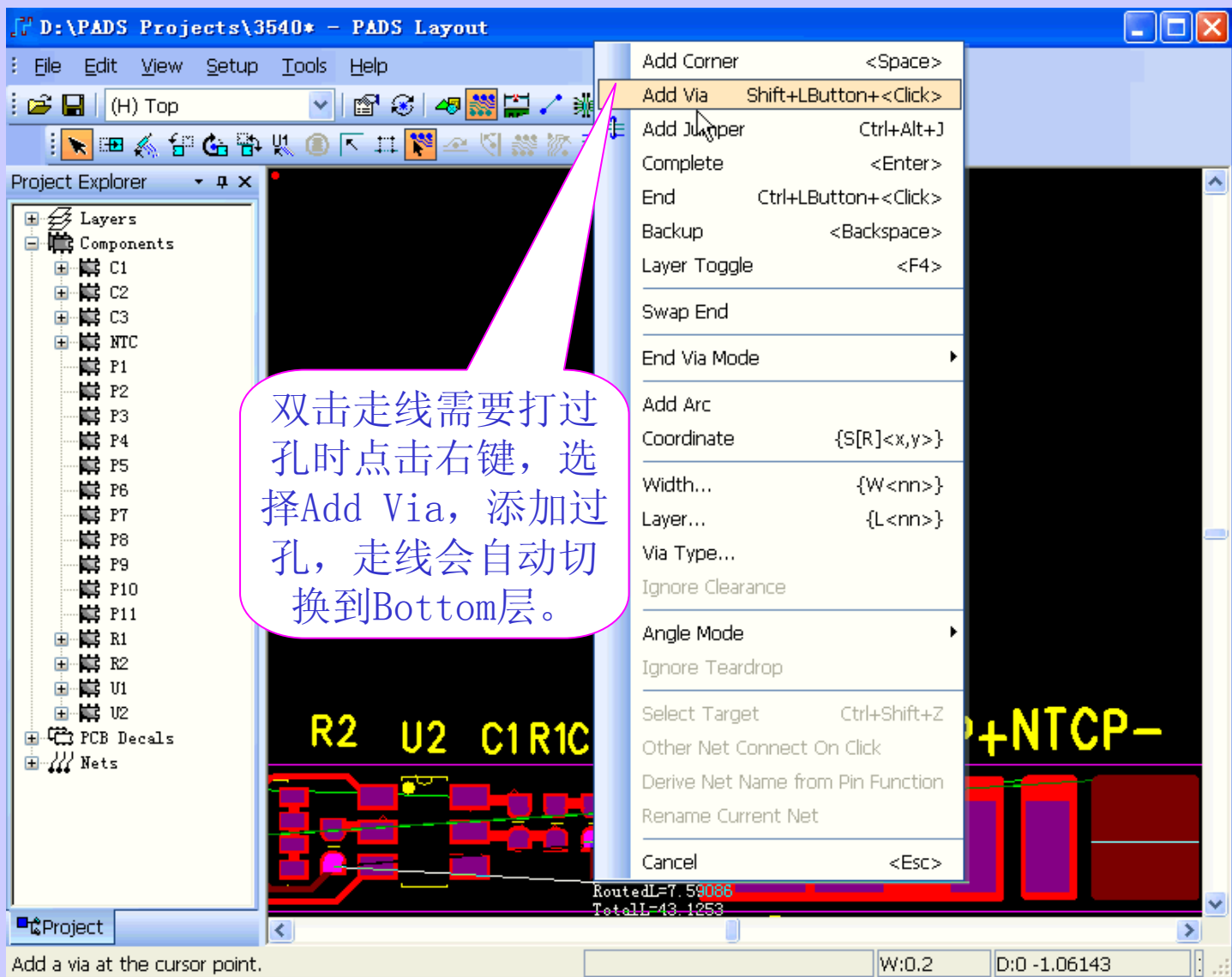
B-

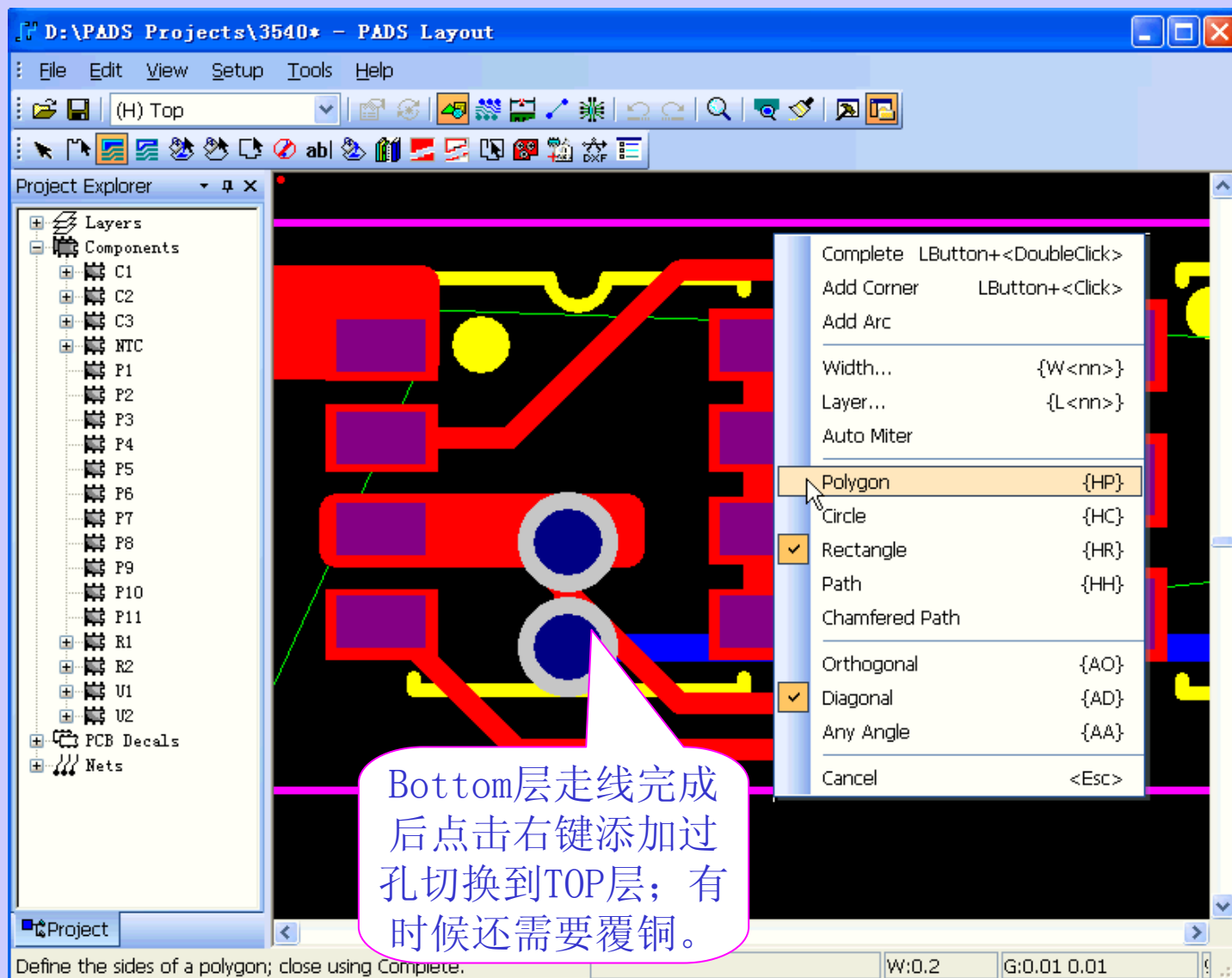
Output Window

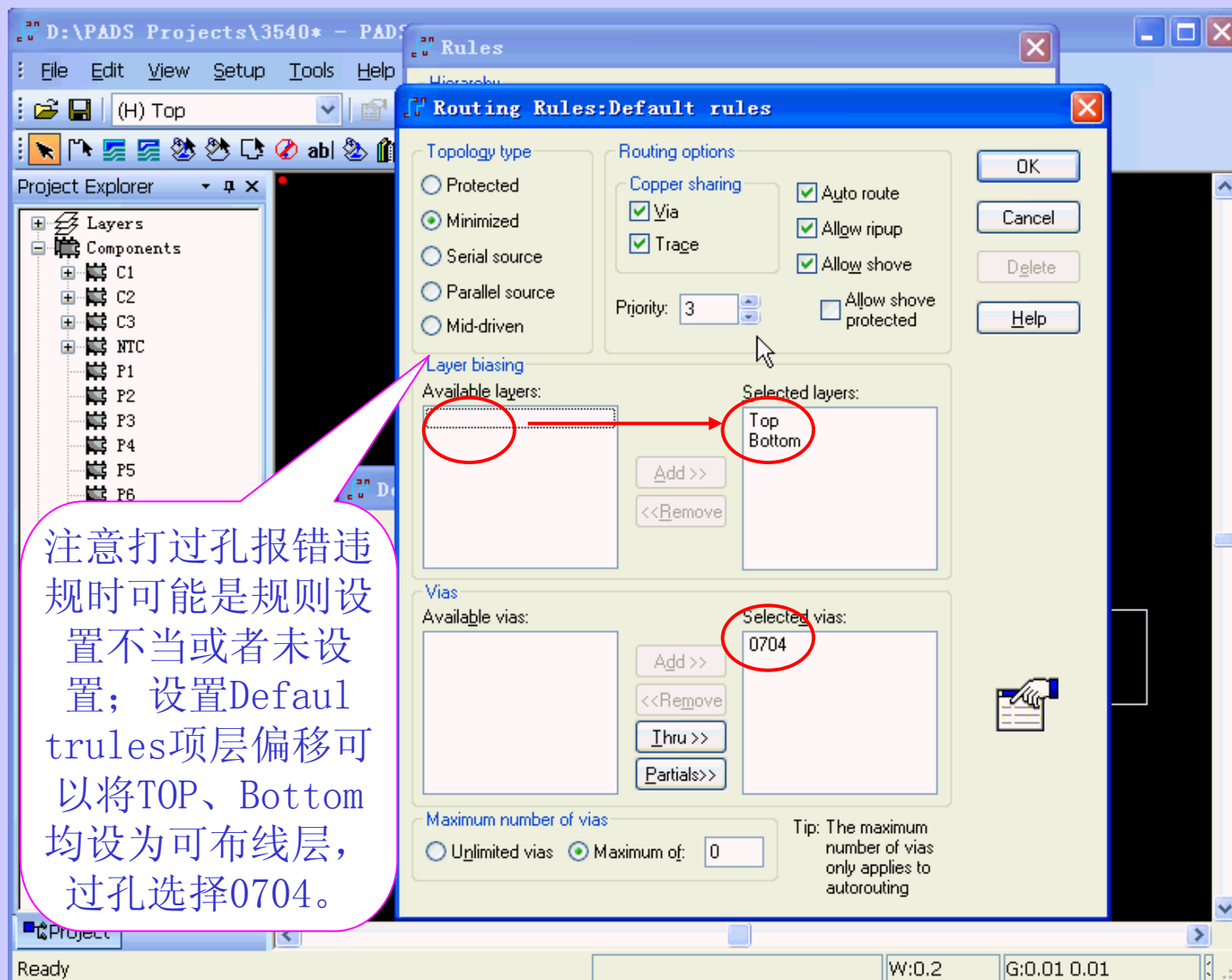
Define pad stacks for components and vias.

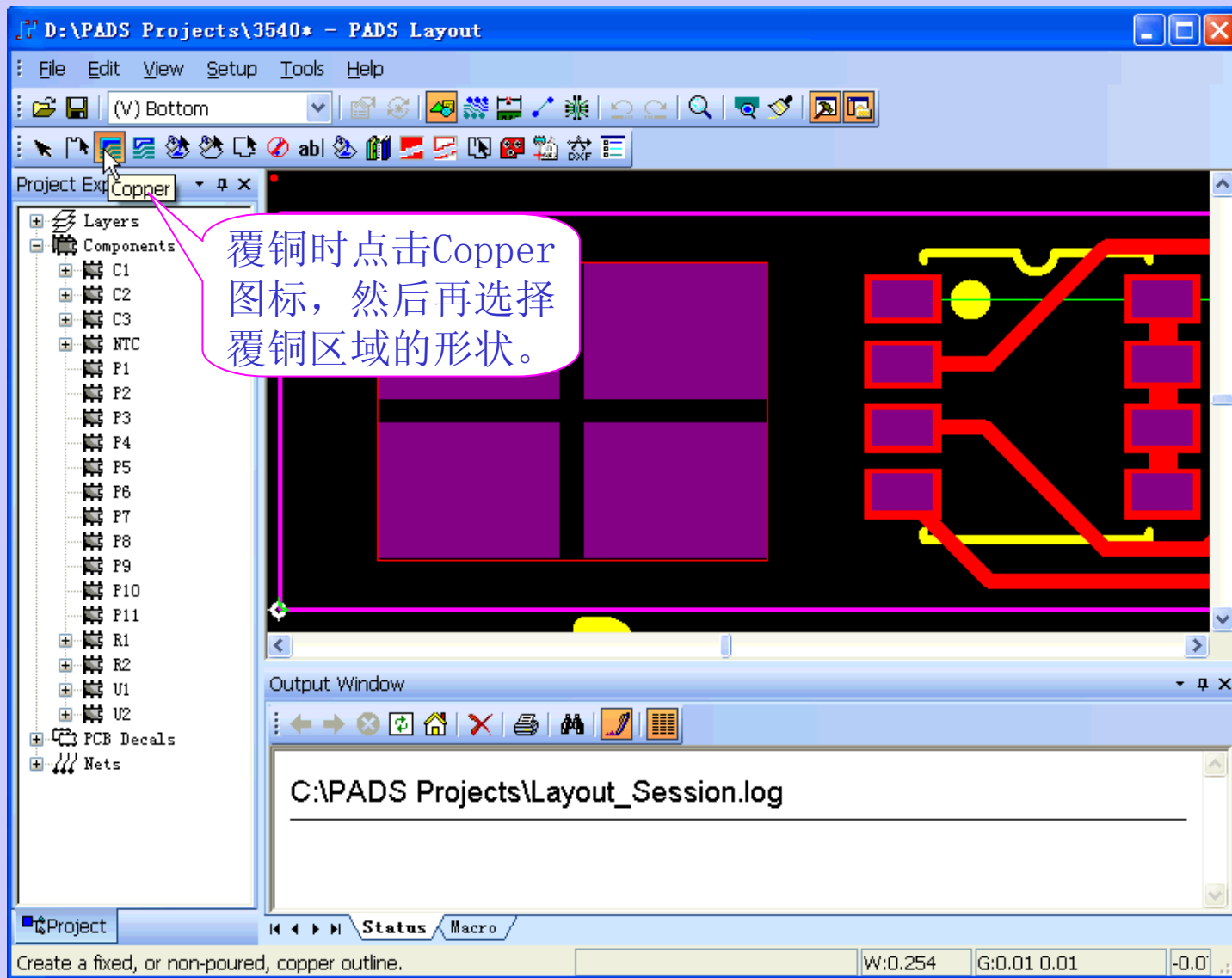
W:0.254 G:0.01 0.01 13.C

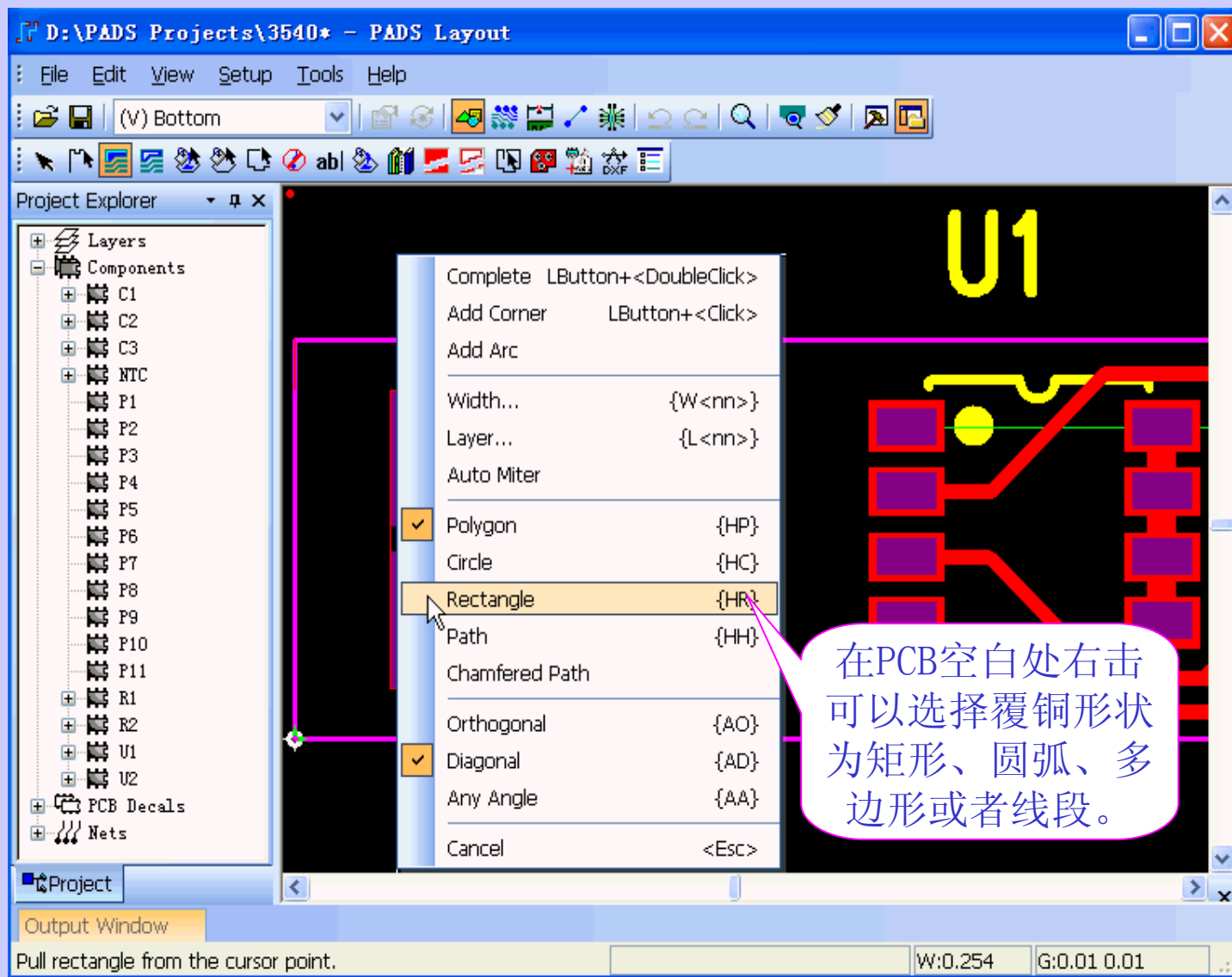


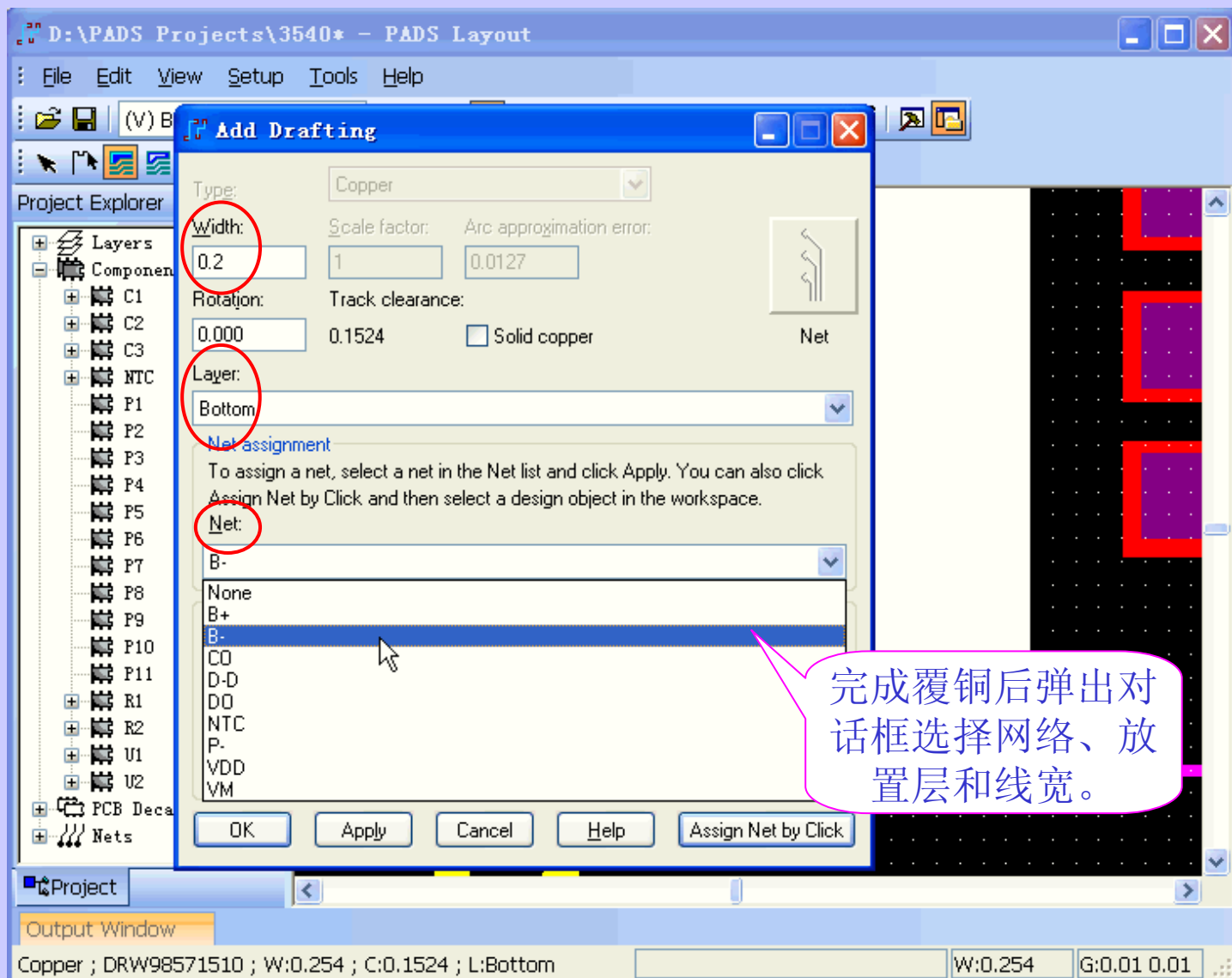


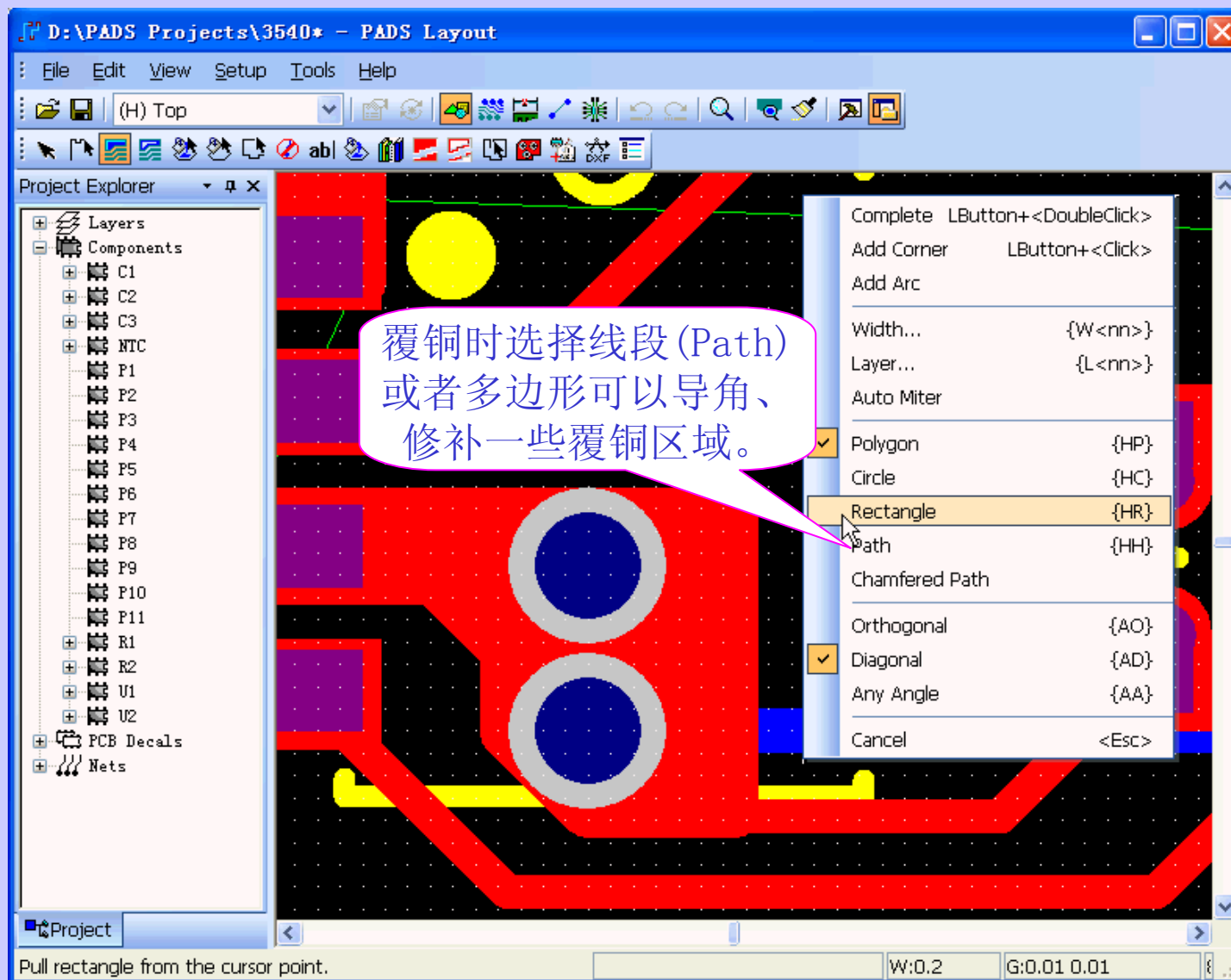


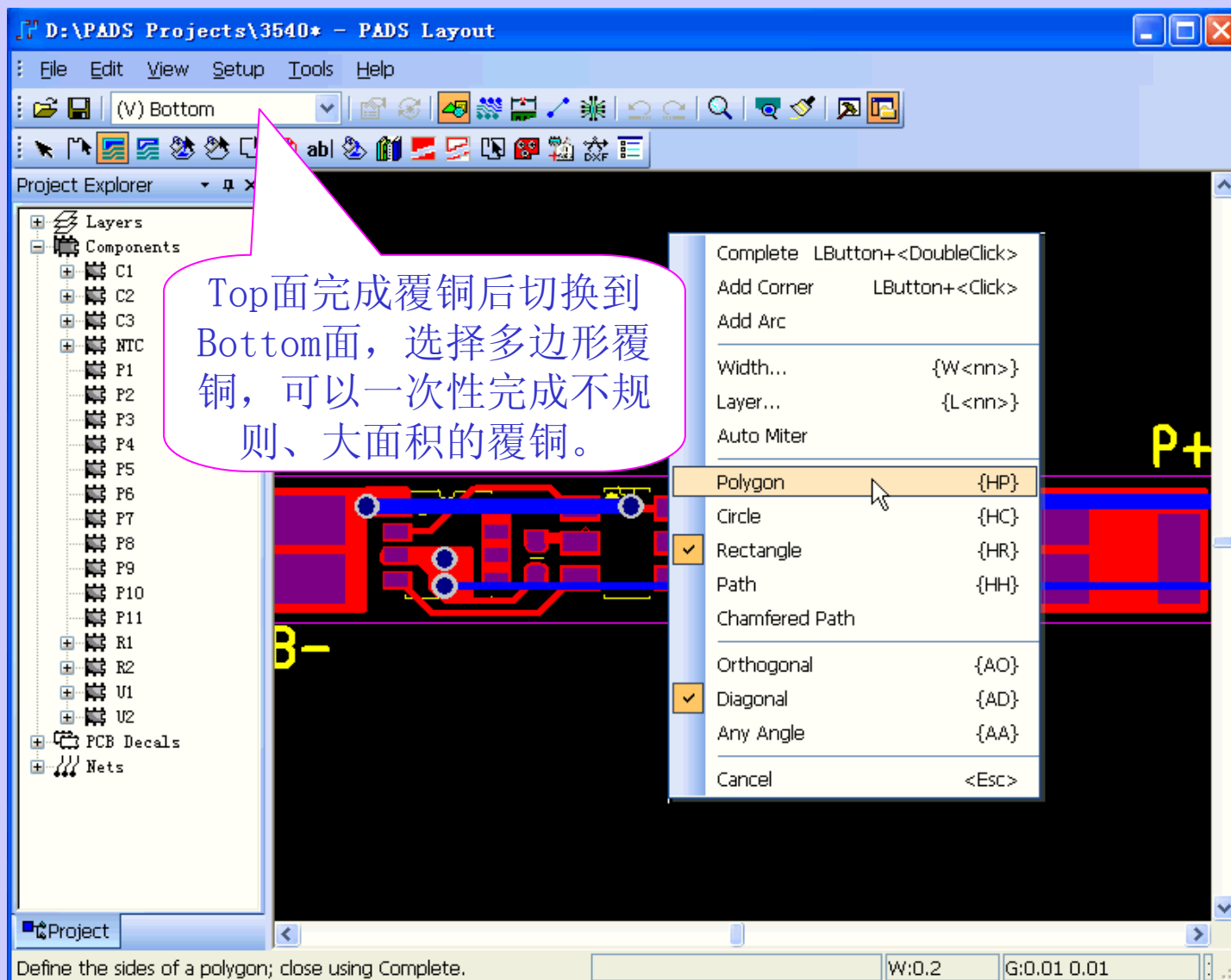


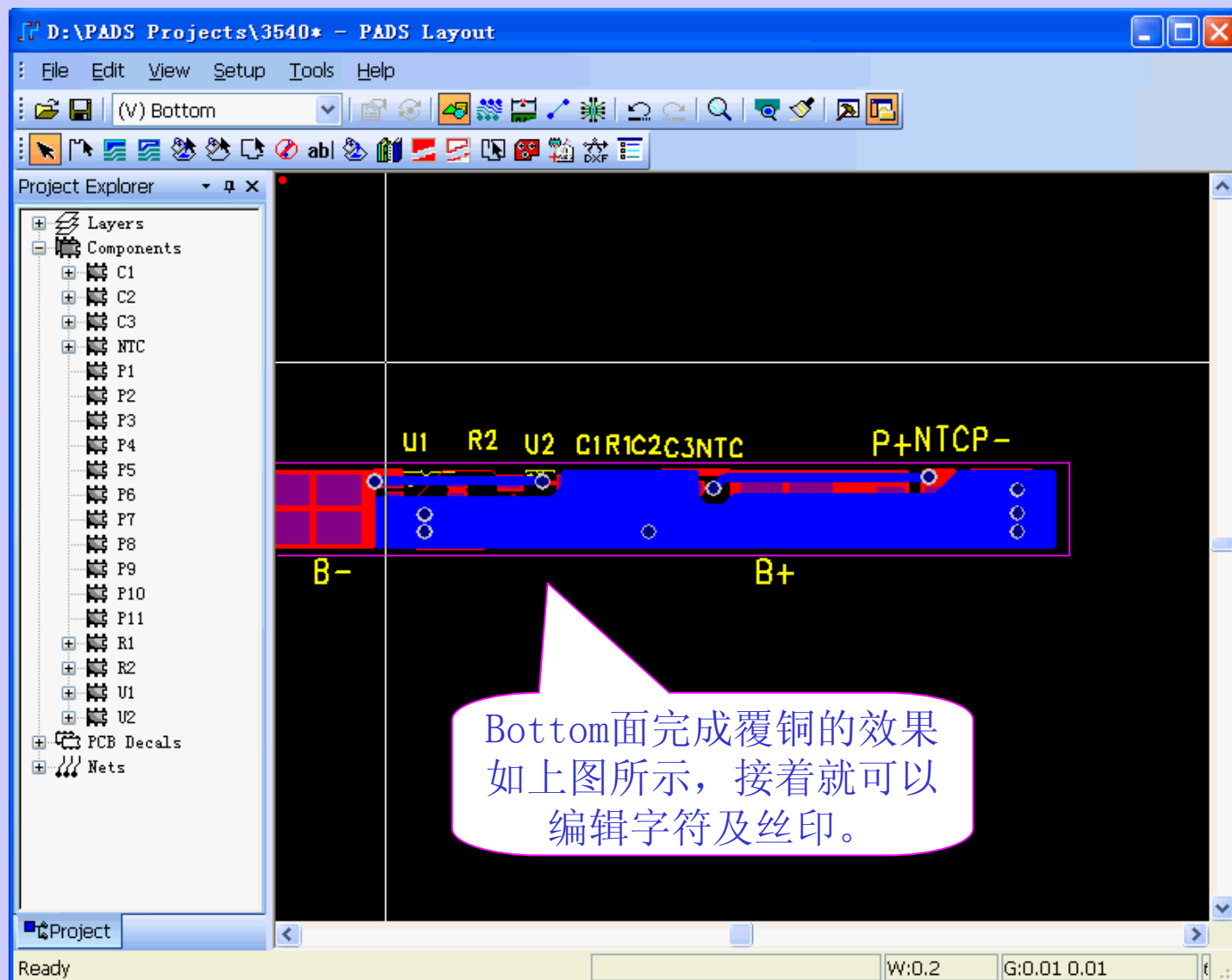






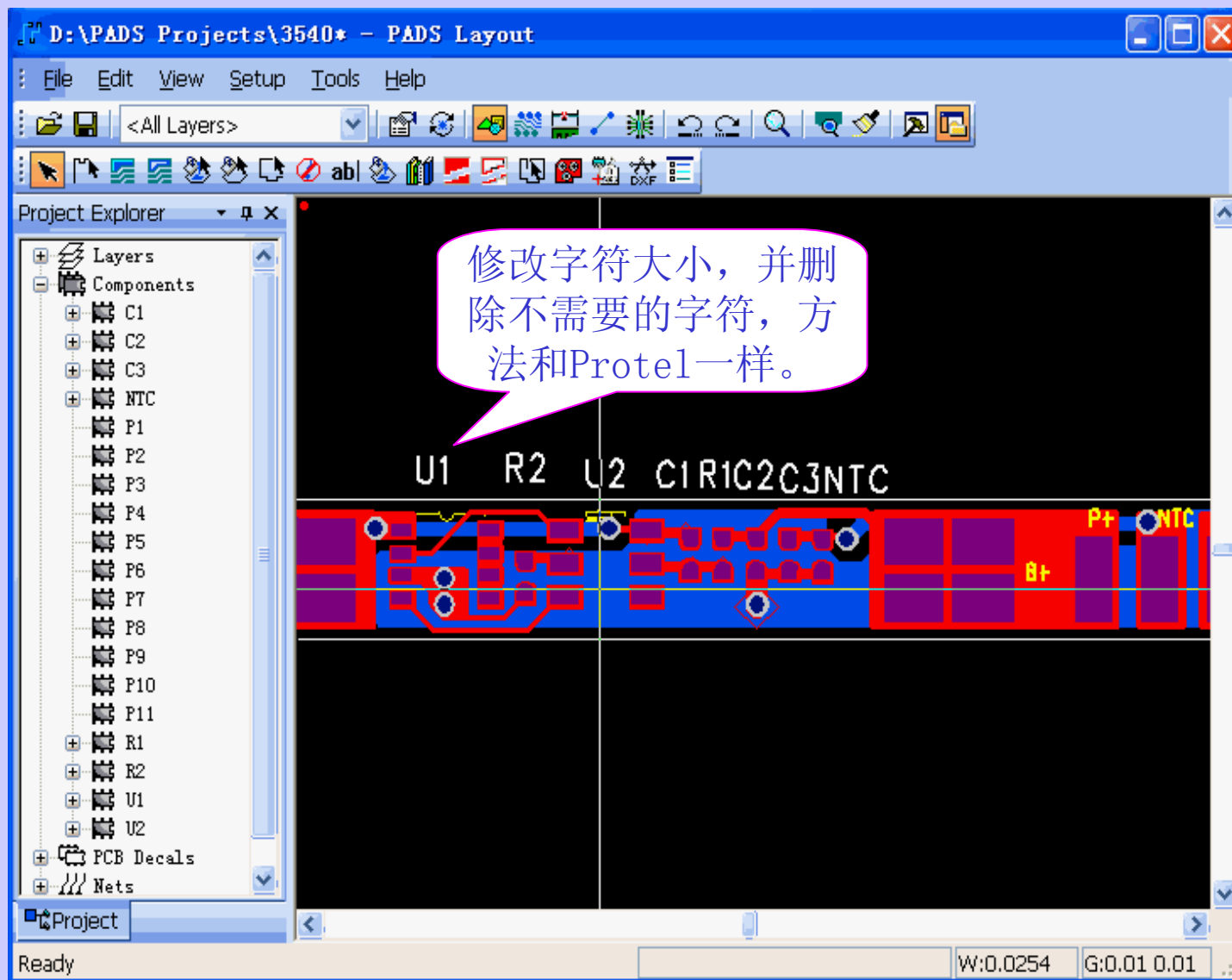


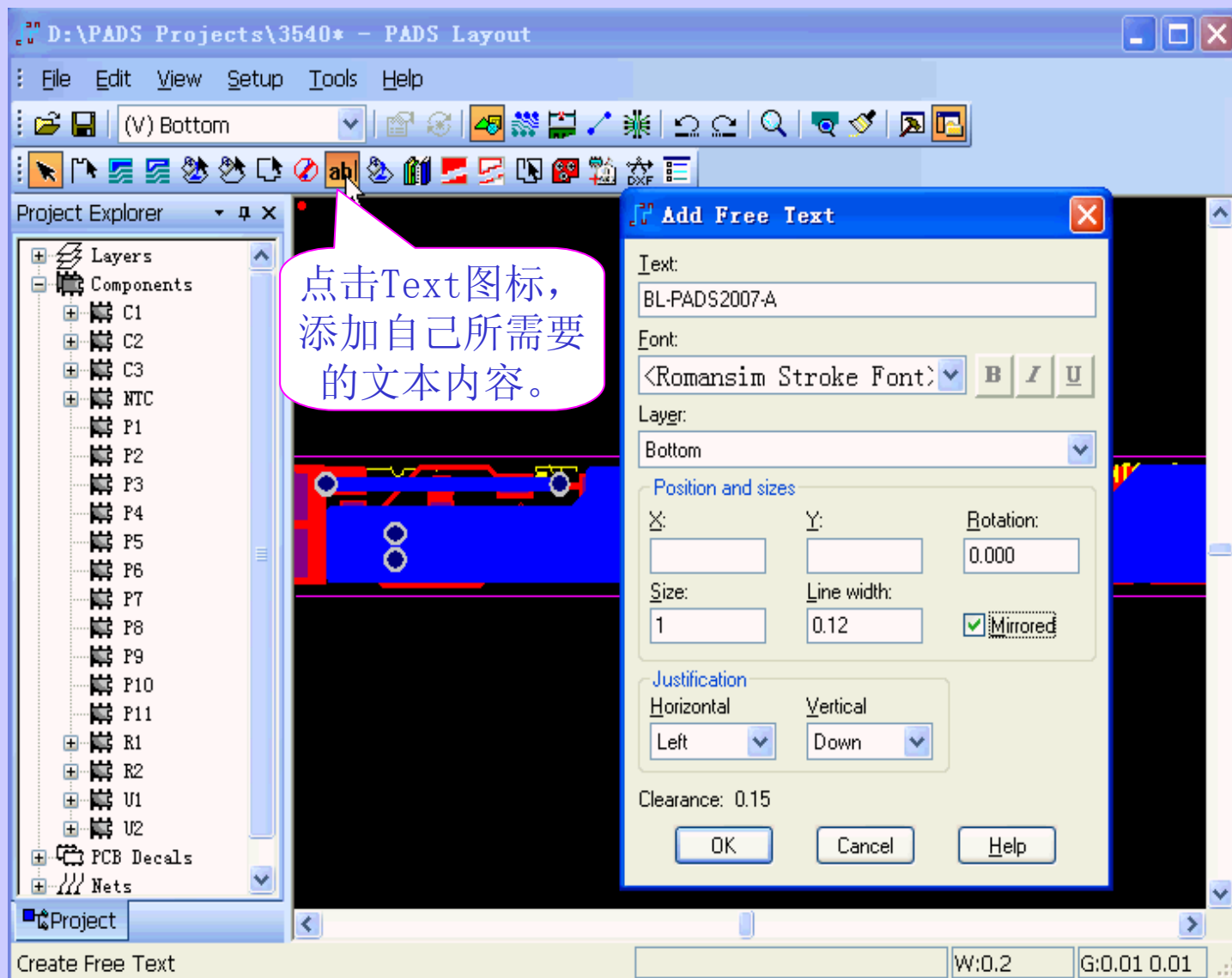


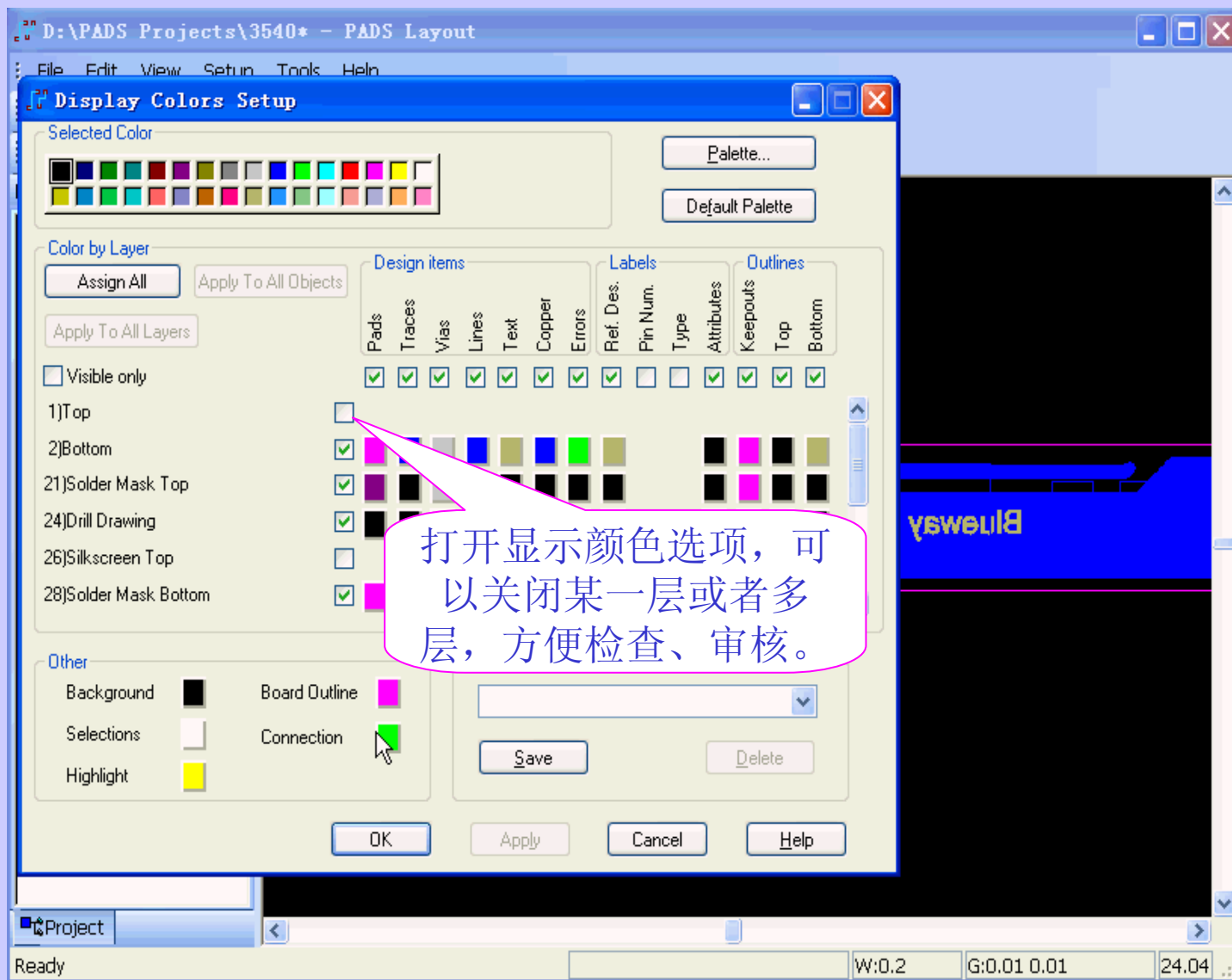


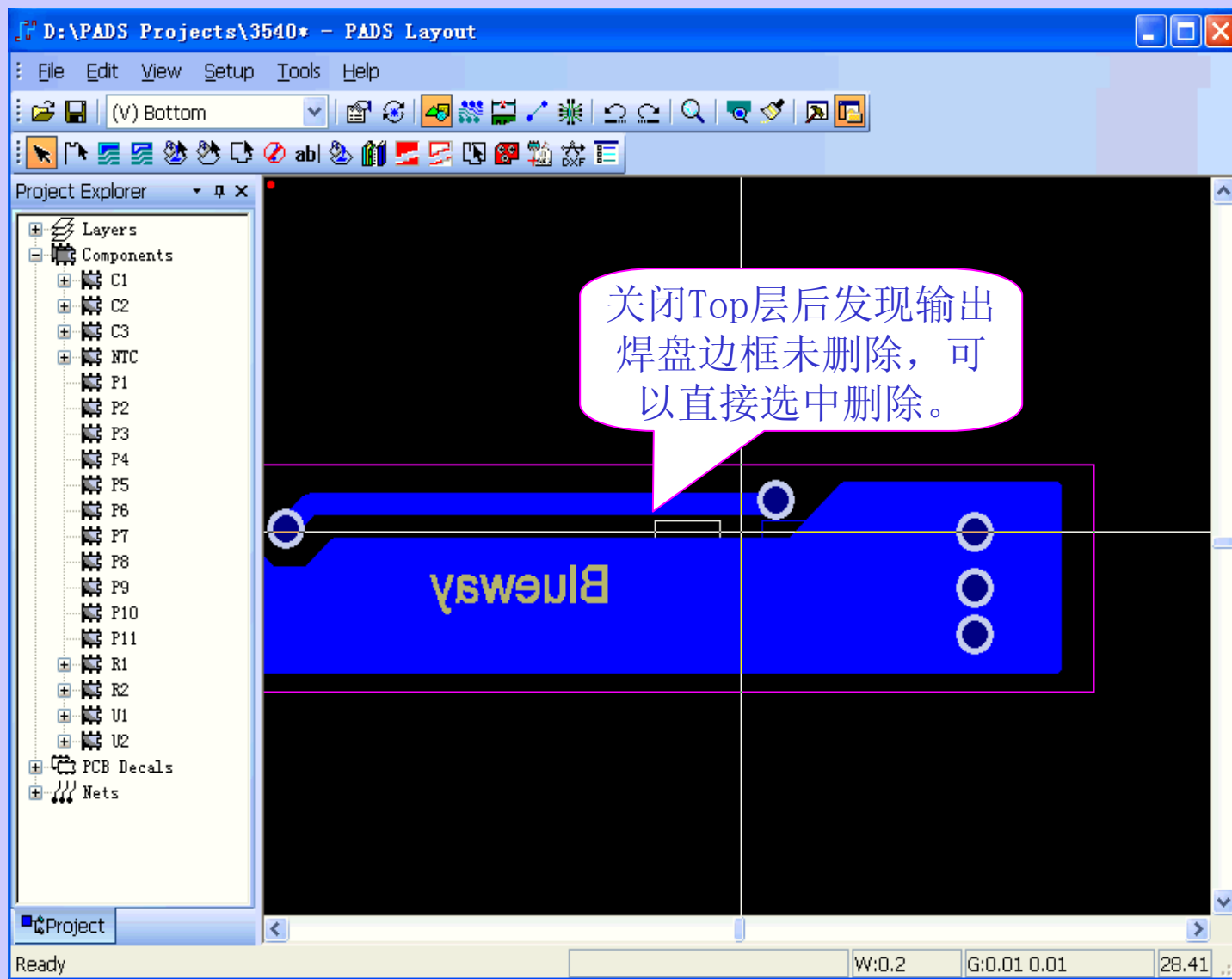


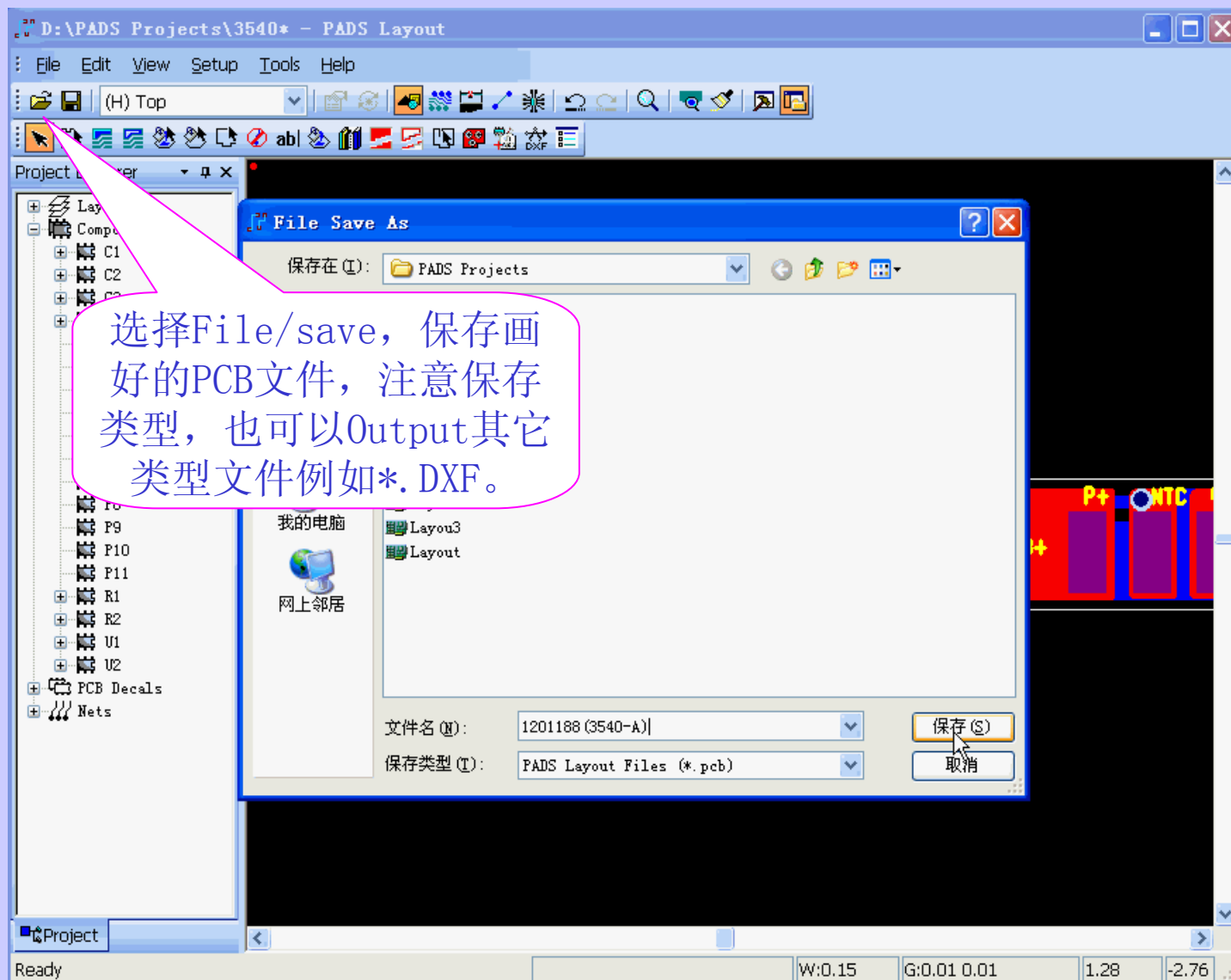
6.检查PCB并输出文件

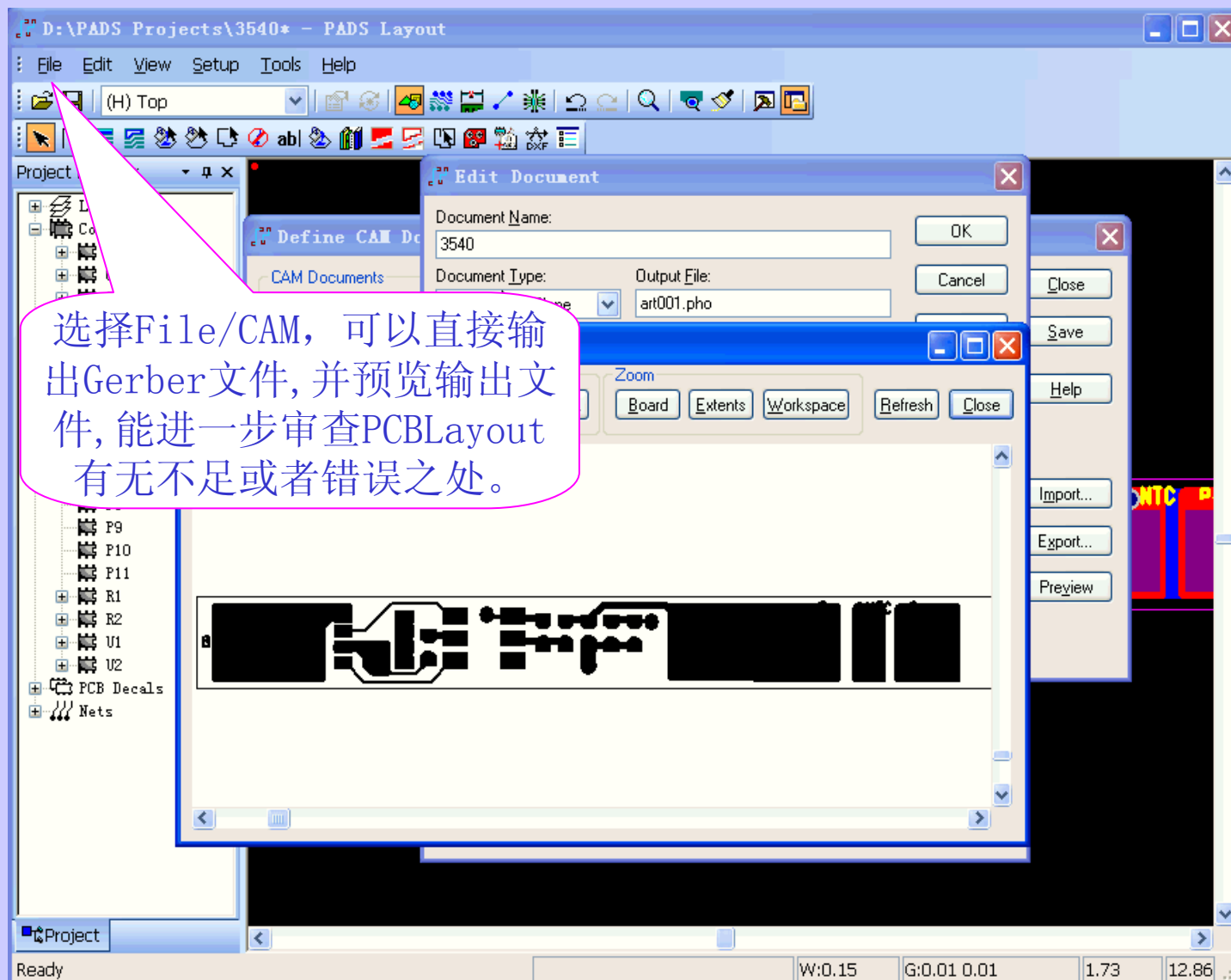






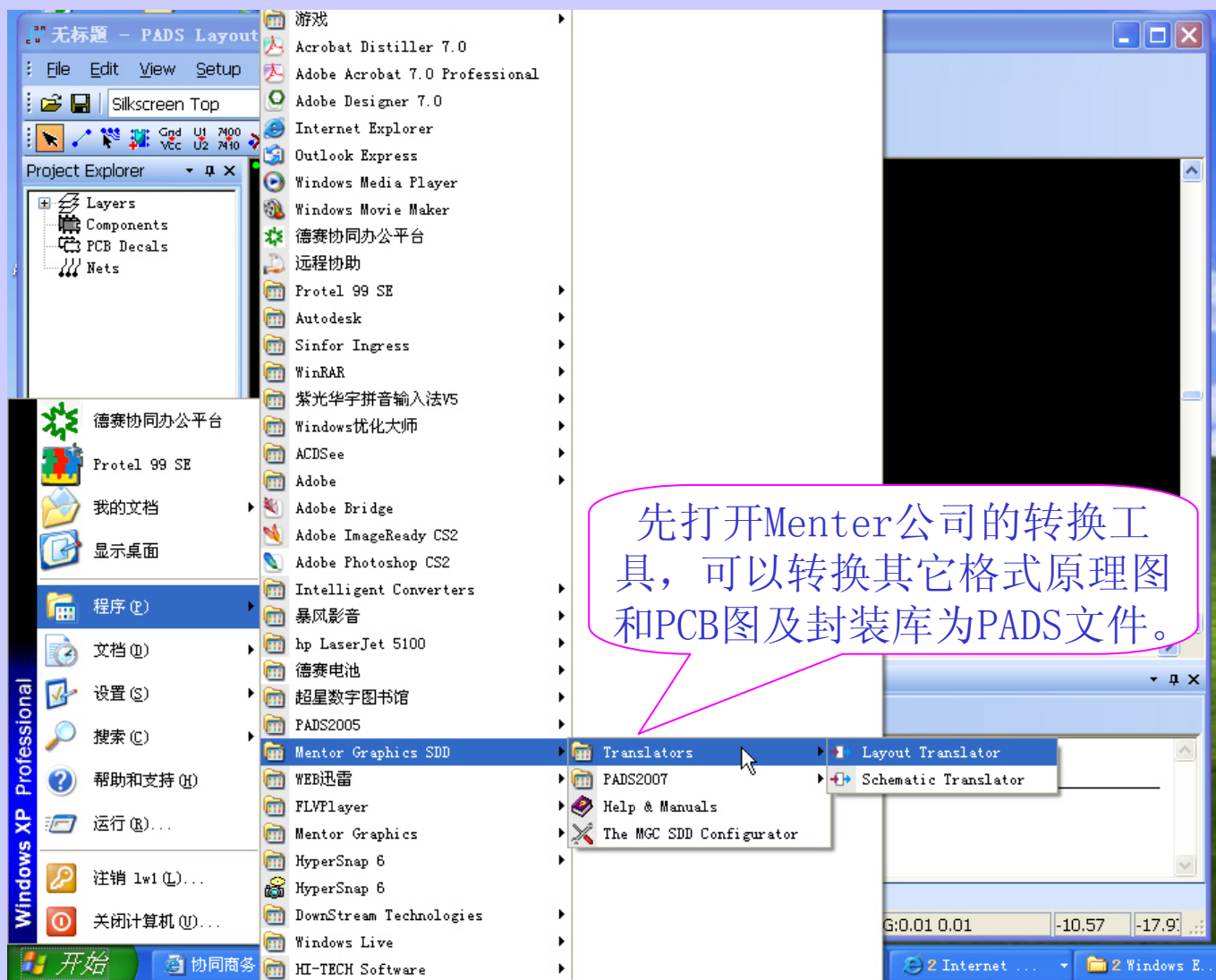


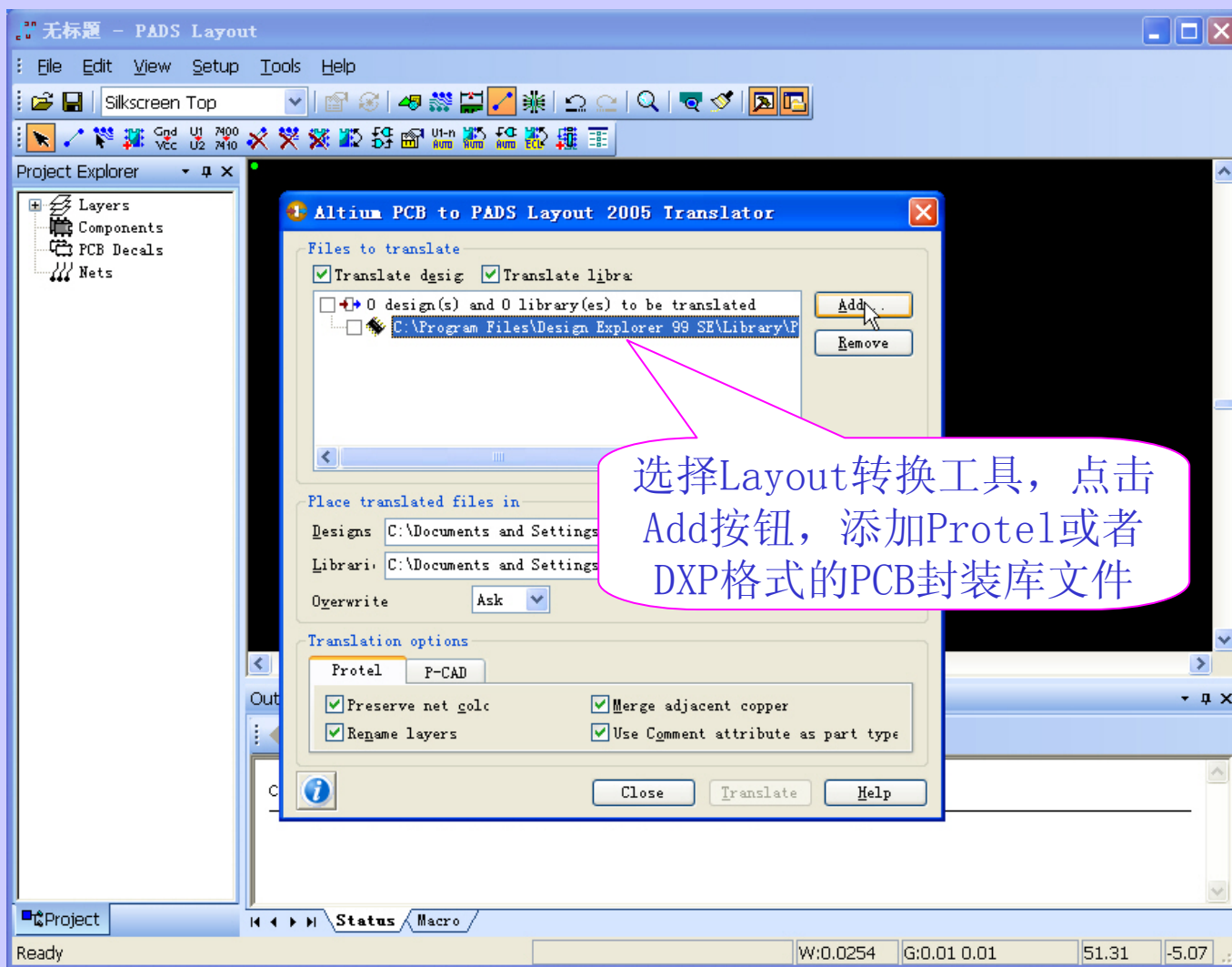


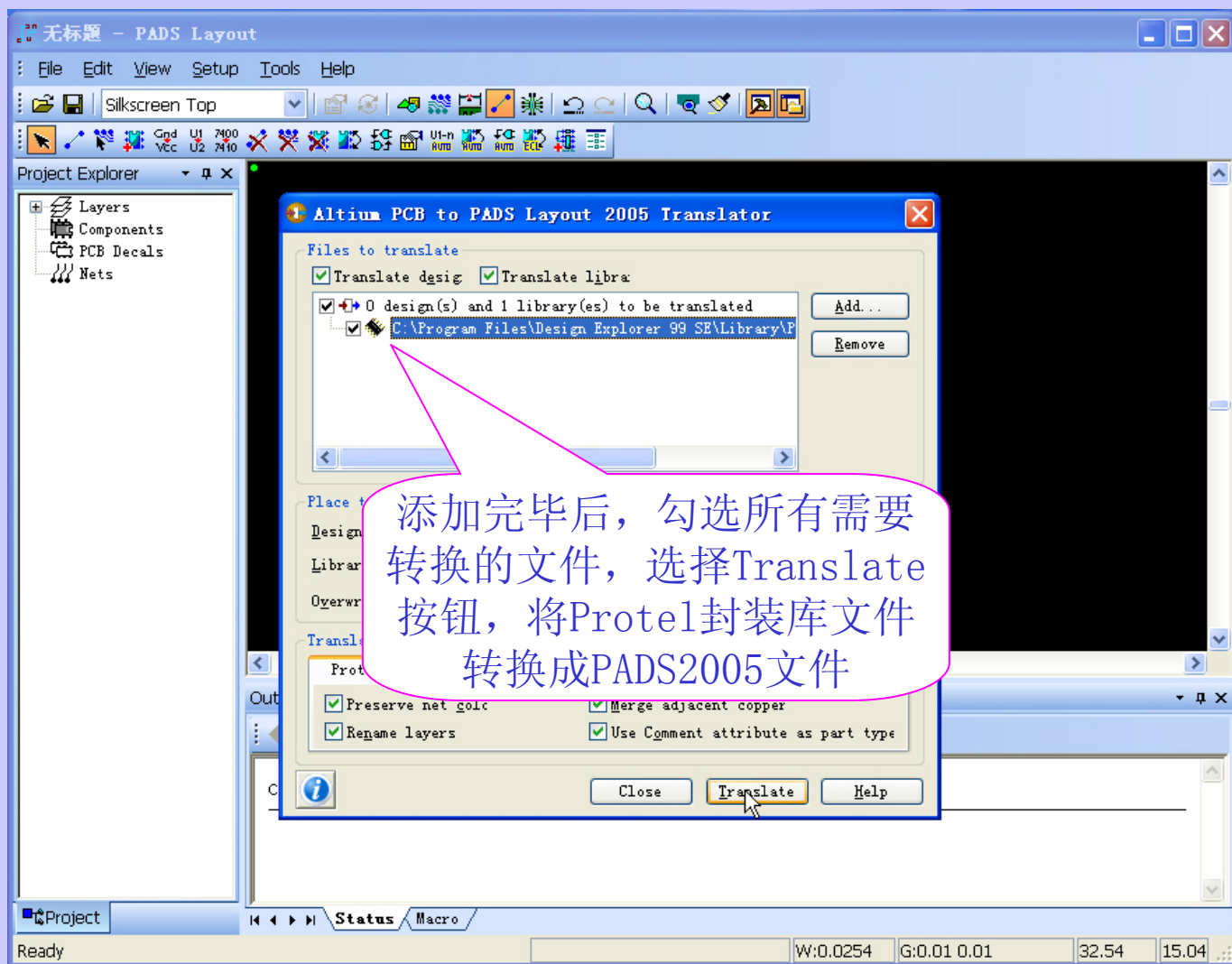


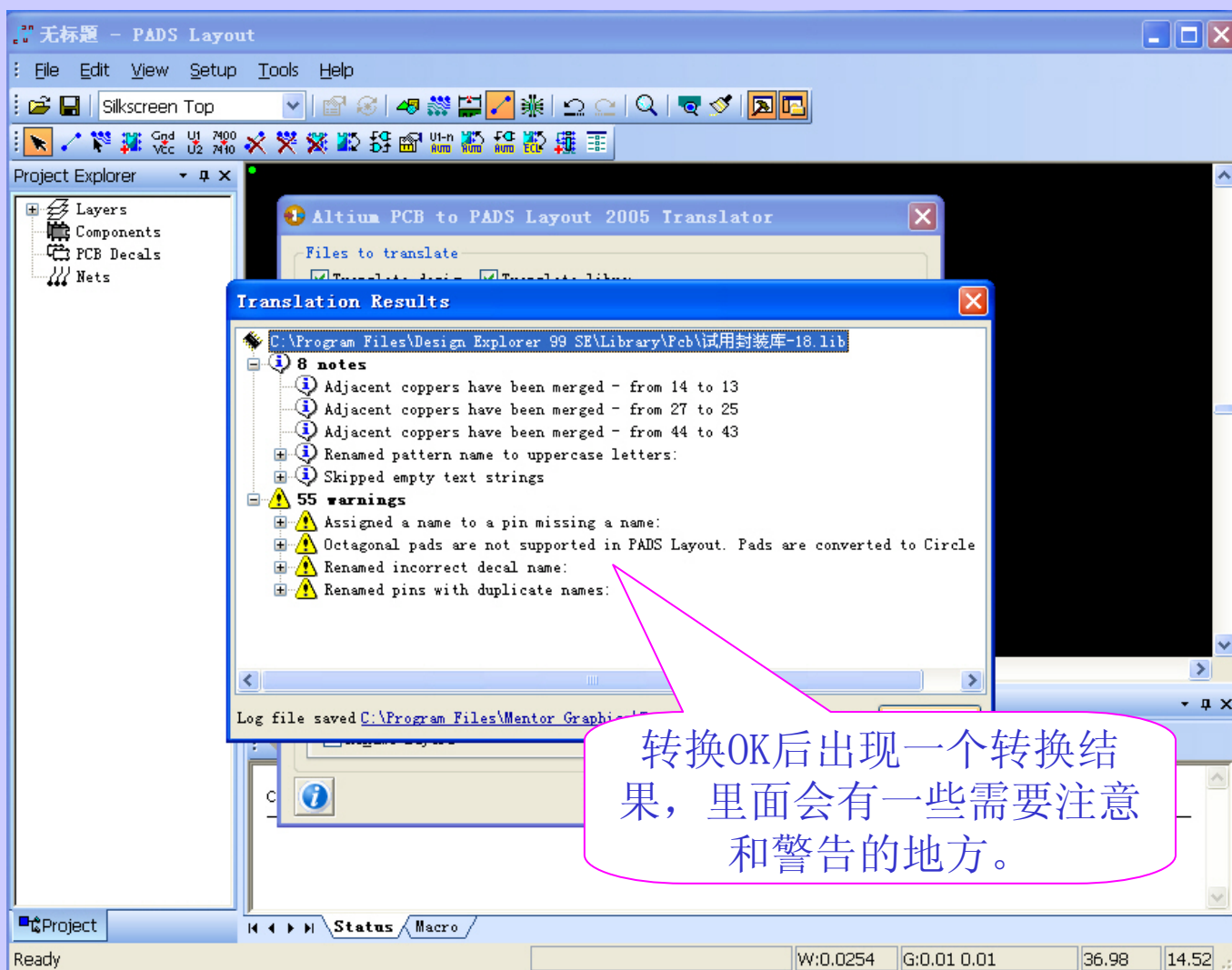


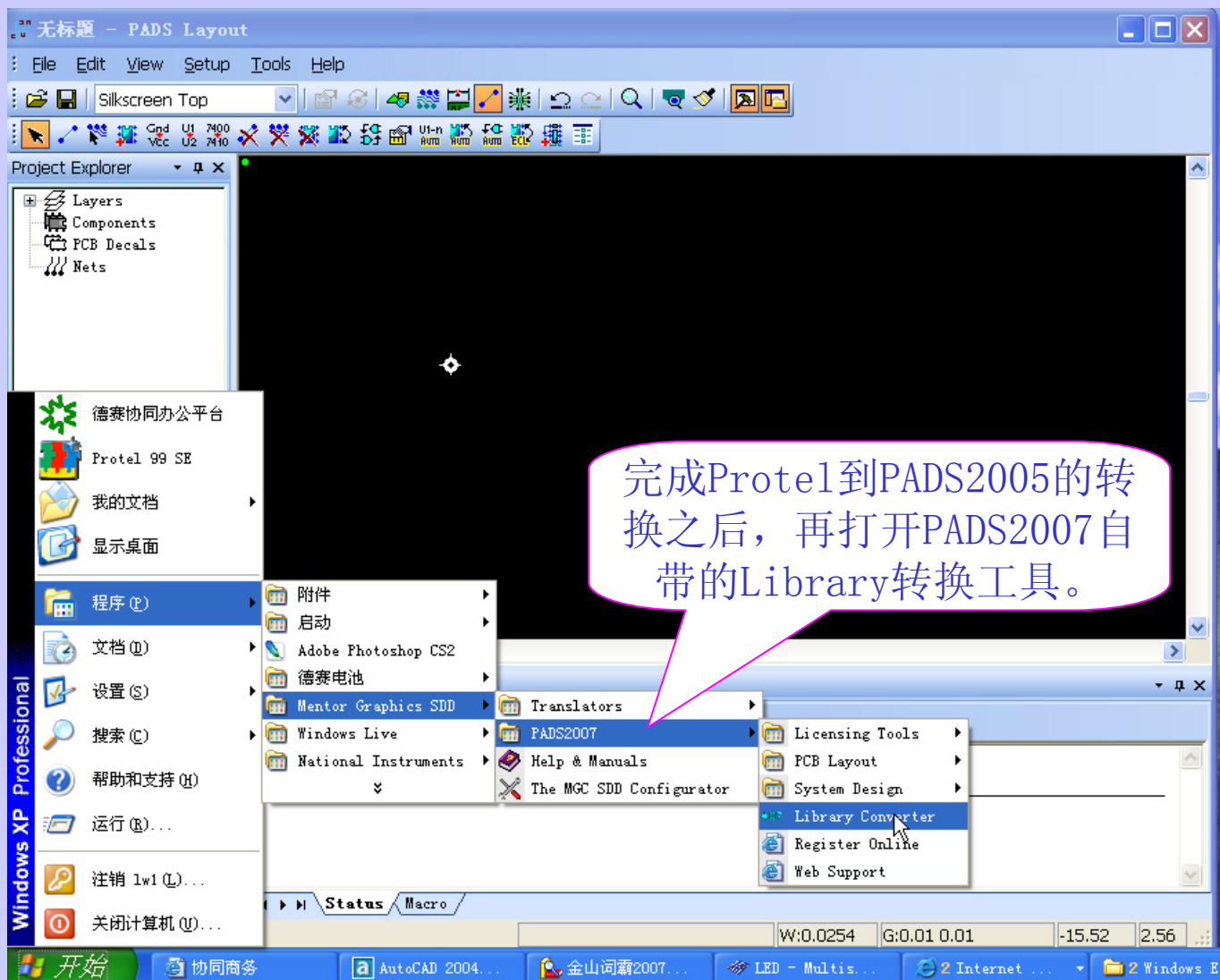
7.Protel 与PADS封装库的转换

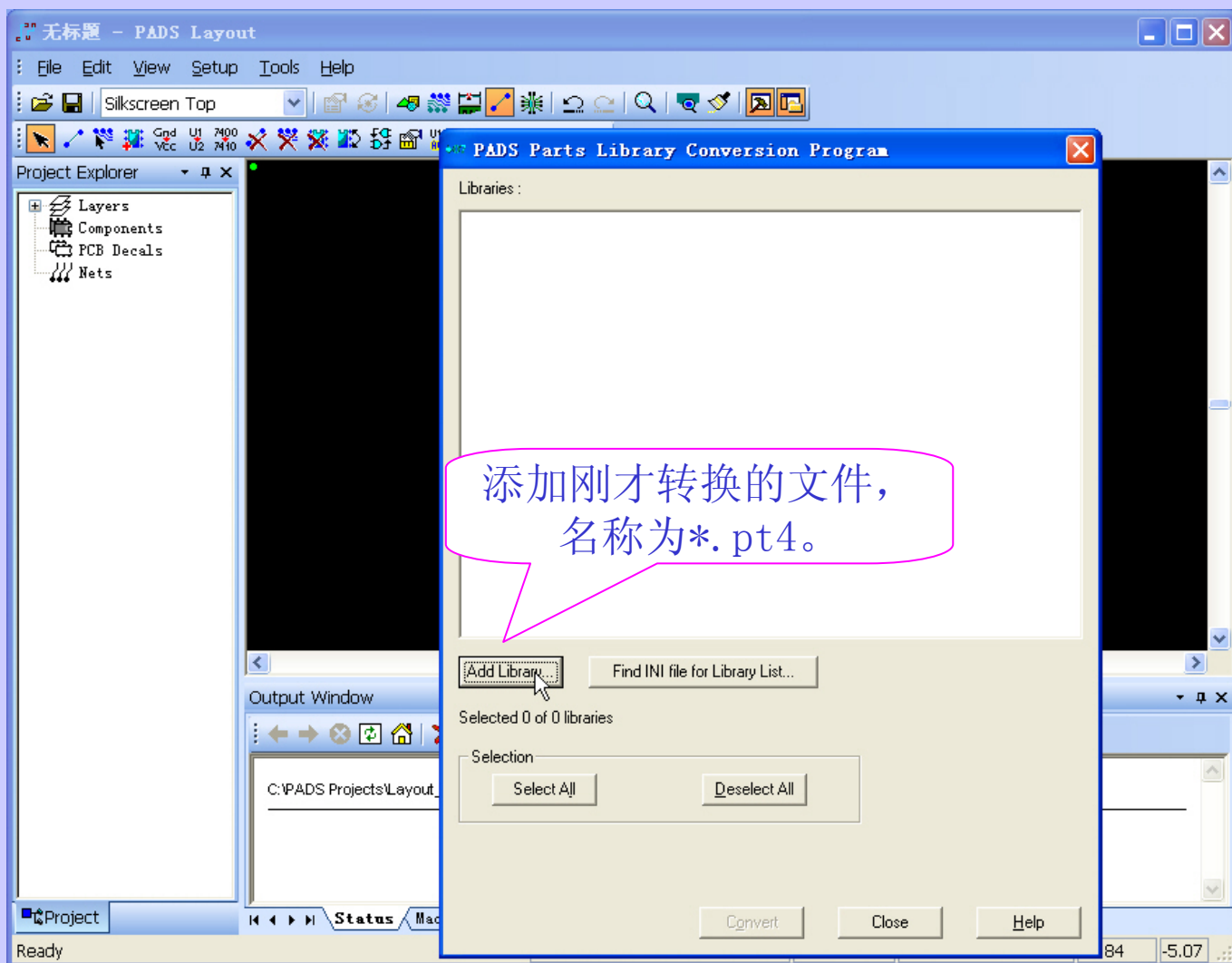


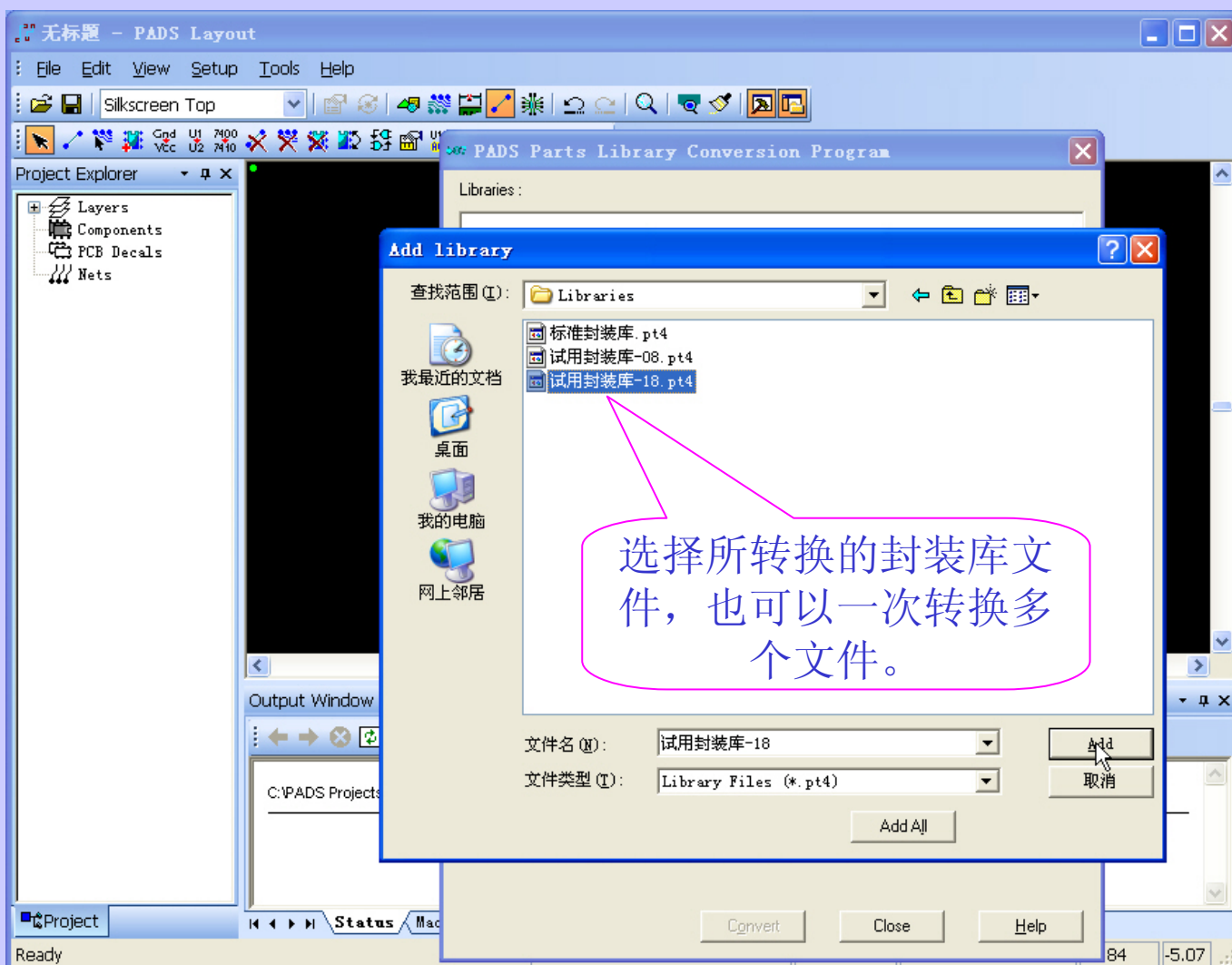


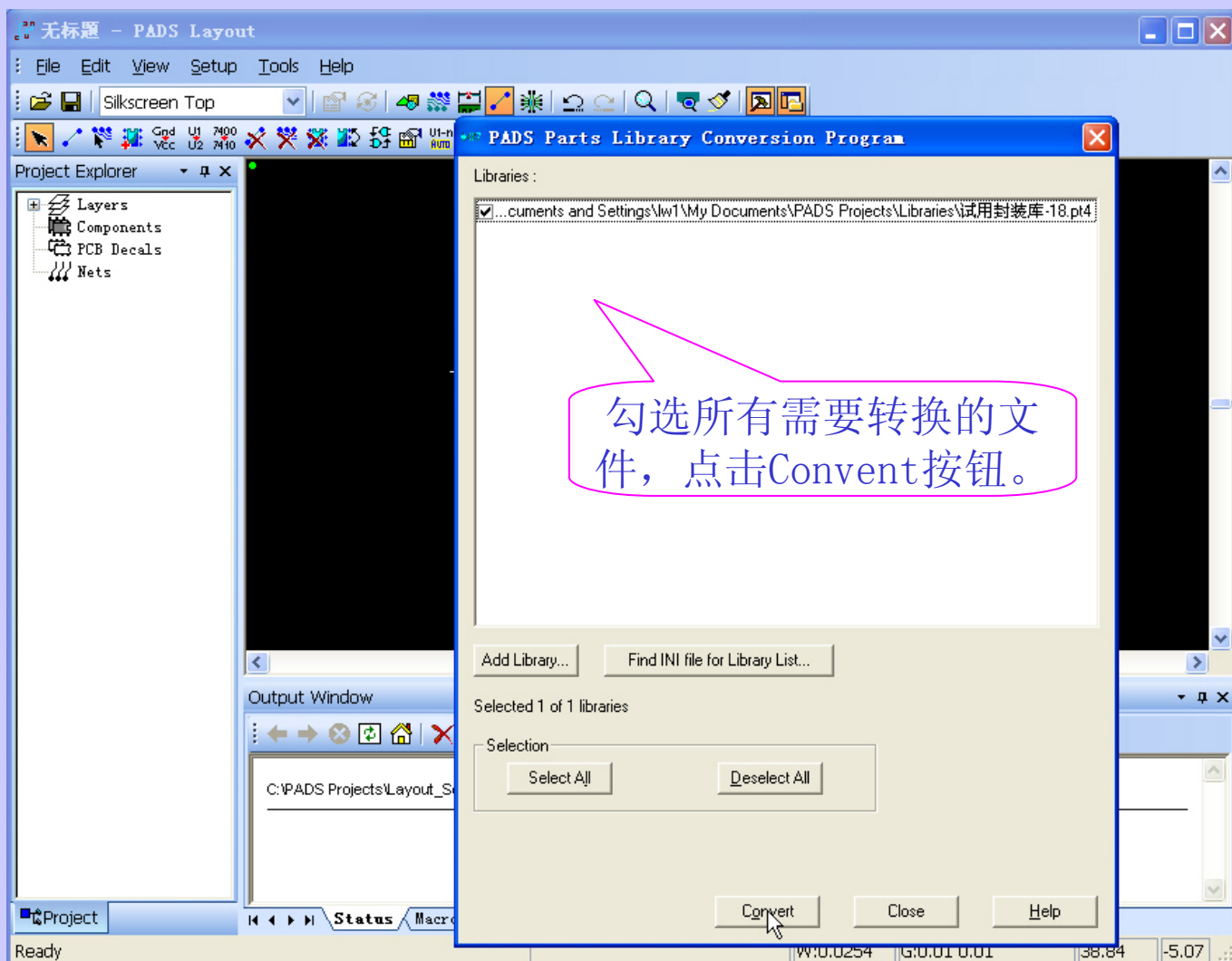


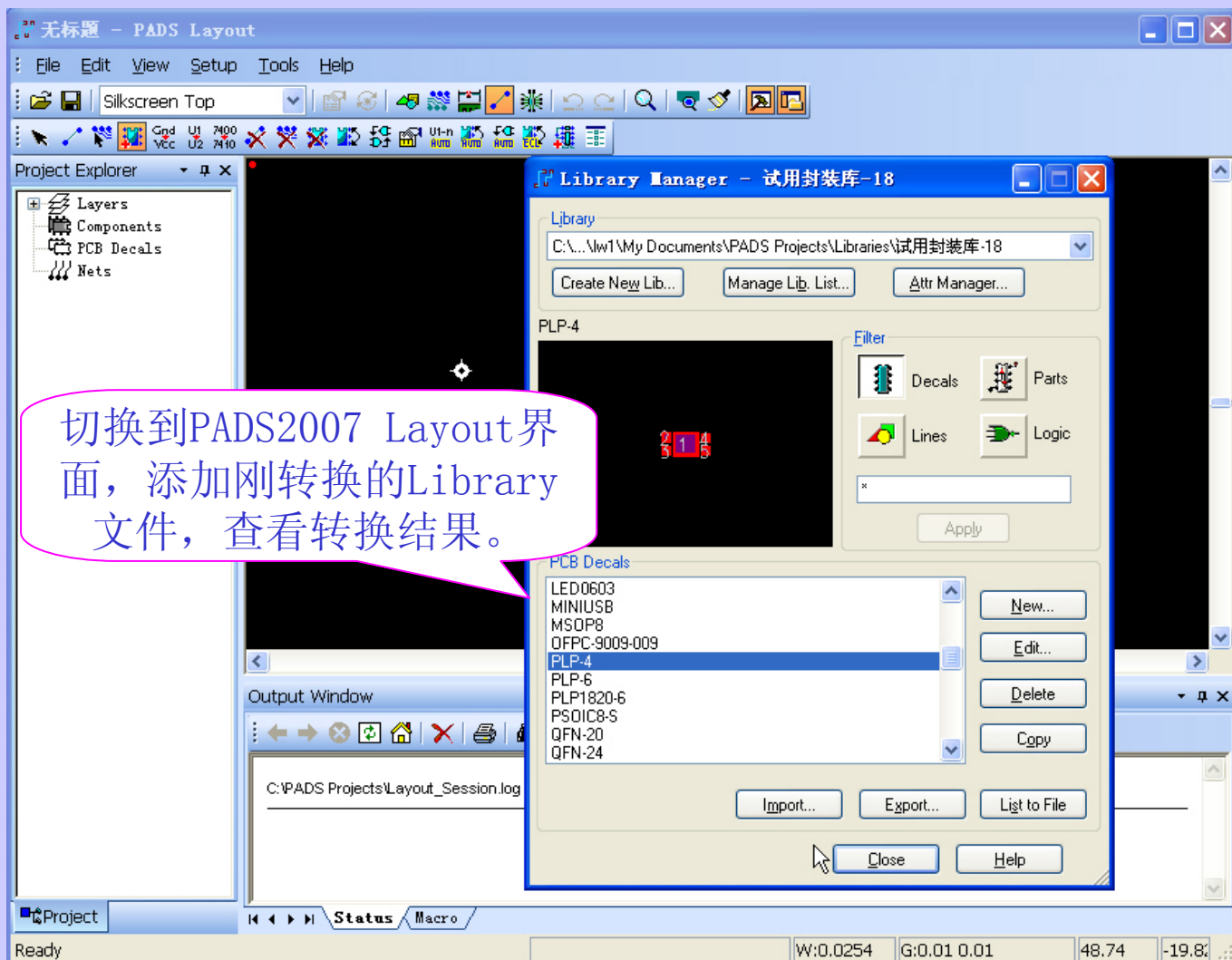














The End, Thanks!